

LIQUID CRYSTAL DISPLAY MODULE

Product Specification

PRODUCT NUMBER	LER4328
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INTERNAL APPROVALS						
Product Manager	Engineering	Document Control				

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REVISION RECORD

Date	Page	Par.	Comment	ECN no.
02/16/09			New DCA Specification	E3992
	02/16/09	02/16/09	02/16/09	02/16/09 New DCA Specification

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1 GENERAL SPECIFICATIONS

ITEM	DESCRIPTION	UNIT
Module Outline Dimensions	87.0 (W) x 71.0 (H) x 15.6 Max (D)	mm
Viewing Area	62.5 (W) x 43.5 (H)	mm
Active Display Area	56.28 (W) x 38.36 (H)	mm
Dot Configuration Format	128 (W) x 64 (H)	dots
Dot Dimensions	0.40 (W) x 0.56 (H)	mm
Dot Spacing	0.04 (W) x 0.04 (H)	mm
LCD Type	STN / Transflective / Positive	
Backlight Type	Array LED / Yellow-Green	
Duty Ratio	1/64	
Bias Drive	1/9	
Controller / Interface	Neotec NT7108 / 8-bit Parallel interface	
Power Supply	Vdd	V
RoHS Complaint	Yes	

2 FEATURES

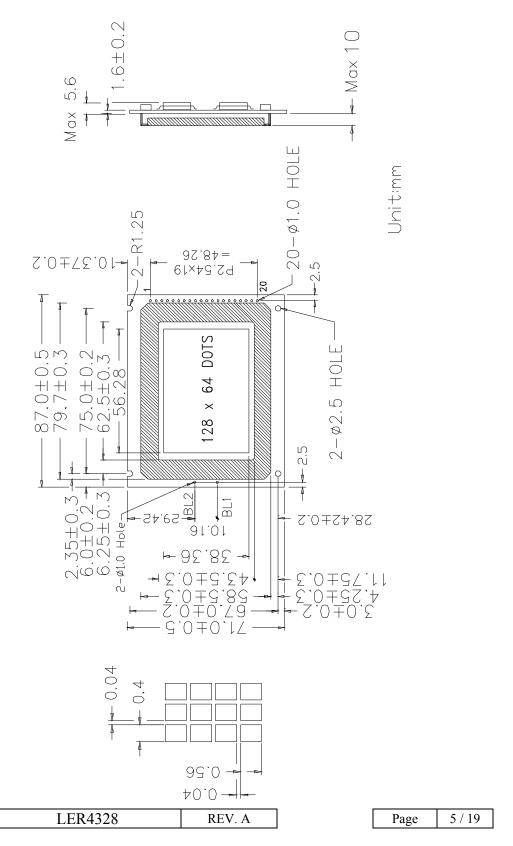
• With Negative Voltage (NV).

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3 MECHANICAL DRAWING

Product No.





4 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Conditions	Min.	Max.	Unit
Power Supply Voltage	Vdd	$Ta = 25^{\circ}C$, $50 \pm 10\%$ RH	0	7.0	V
Operating Temperature	Tonr	< 65% RH (Normal Temp.)	0	50	°C
Operating Temperature	Topr	< 65% RH (Wide Temp.)	-20	70	
		< 65% RH (Normal Temp.)		70	°C
Stone on Tomas anothers	Tata	< 65% RH (Wide Temp.)	-30	80	C
Storage Temperature	Tstg	< 48 hrs		90	% RH
		< 1000 hrs	20	65	% RH

5 ELECTRONIC CHARACTERISTICS

5.1 DC CHARACTERISTICS

 $(Vss = 0 V, Ta = 25^{\circ}C)$

Item		Symbol	Conditions	Min.	Typ.	Max.	Unit
Operating V	oltage	Vdd		4.75		5.25	V
Input	High	Vihc		0.7Vdd		Vdd	V
Voltage	Low	Vilc		0		0.8	V
LCD Driving	CD Driving Voltage Vdd - Vo		8		17.0	V	

5.2 LCD CURRENT CONSUMPTION & DRIVING VOLTAGE

(Vdd - Vss = 5.0 V)

			STN TEMPERATURE		
			Normal Temp.	Wide Temp.	
Supply Current, (Idd) Typ., mA		10	N/A		
Supply Curre	nt, (Iee) Typ., mA		N/A	N/A	
Recommended L			CD Driving voltage		
I CD	Ta = -20 °C		N/A	N/A	
LCD Driving Voltage (Vdd – Vo)	Ta = 0 °C		14.4	N/A	
	Ta = 25 °C		14.6	N/A	
	Ta = 50 °C		14.8	N/A	
(Ta = 70 °C	·	N/A	N/A	

Note: Negative Voltage (NV) is built-in

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6 OPTOELECTRONIC CHARACTERISTICS

 $(Ta = 25^{\circ}C)$

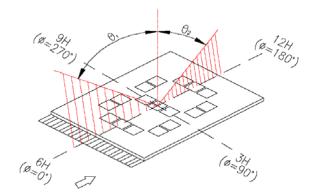
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit	Note
	θ1 (down)	$CR \ge 2$		30		deg.	6.1
Viewing Angle	θ2 (up)	$CR \ge 2$		20		deg.	6.1
Viewing Angle	θ3 (right)	CR ≥ 2		30		deg.	6.2
	θ4 (left)	CR ≥ 2		30		deg.	6.2
Contrast Ratio	CR	Ta = 25°C	3.0				6.3
Dagnanga Tima	Tr	Ta = 25°C			300	122 G	6.4
Response Time	Tf	Ta = 25°C			300	ms	0.4
Viewing Direction		6 O'clock					

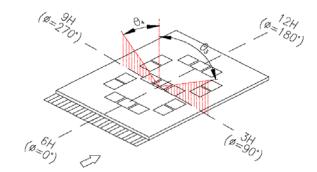
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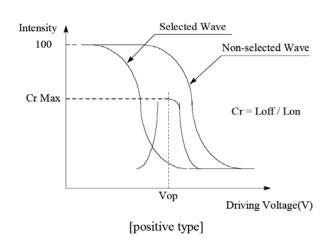


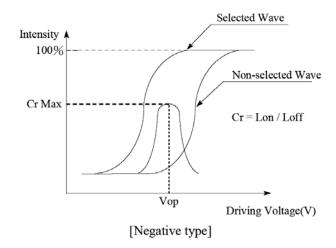
Note 6.1: Definition of Viewing angle, θ1 & θ2 Note 6.2: Definition of Viewing angle, θ3 & θ4



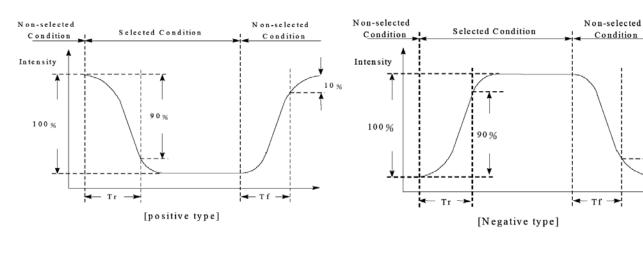


Note 6.3: Definition of Contrast Ratio (CR)





Note 6.4: Definition of Response Time



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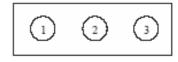
7 ARRAY LED BACKLIGHT ELECTRICAL CHARACTERISTICS

Item	Conditions	Min.	Typ.	Max.	Unit
Input voltage	$Ta = 25^{\circ}C$		5.0	ŀ	Vrms
Current consumption	$Ta = 25^{\circ}C$		260		mA
Average brightness	Test when connected for	3 min., T	$a = 25 ^{\circ}C$		cd/m²
(B/L only)(Ta = 25°C, IL = 260 mA)	Yellow-Green Array LED B/L		110		(Note 7.1)
Peak Emission Wavelength	Ta = 25°C, $IL = 260 mA$	565	570	575	nm
Brightness uniformity	Ta = 25°C, $IL = 260$ mA	80		-	% (Note 7.2)
Lamp life	Ta = 25°C, IL = 260 mA Humidity: 30% RH ~ 85% RH		50,000	1	Hrs (Note 7.3)
Operating Temp.	Humidity: 30% RH ~ 85% RH	-20		70	°C
Storage Temp.	Humidity: 30% RH ~ 85% RH	-30		80	°C
Limit Resistor (R2)	Ta = 25°C		4.5		Ohm (Note 7.4)

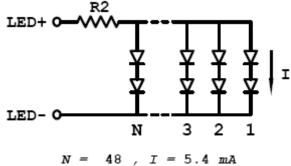
Note 7.1: Average brightness of 3 points when B/L is used at the beginning.

Note 7.2: Brightness uniformity = $(MIN / MAX) \times 100 \%$.

Note 7.3: Half of the original average brightness.



Note 7.4: The limit resistor R2 of LED Backlight is built-in to the LED board.



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8 RELIABILITY TEST

8.1 RELIABILITY CHARACTERISTICS (NORMAL TEMP.)

Test Item	Test Condition	Remarks
High Temperature Operation	240 HR , 50°C ± 2°C	No abnormalities in function and appearance
Low Temperature Operation	240 HR , 0°C ± 2°C	No abnormalities in function and appearance
Thermal Shock Storage (NO operation state)	-20 °C (30 min.) → 25 °C (5 min.) → 70 °C (30 min.) → 25 °C (5 min.) 5 cycles	No abnormalities in function and appearance
Vibration (No operation state)	10 Hz ~ 55 Hz 0.3 mm / 1 Octave 55 Hz ~ 500 Hz 3g / 1 Octave 20 cycles per axis	No abnormalities in function and appearance

8.2 RELIABILITY CHARACTERISTICS (WIDE TEMP.)

Test Item	Test Condition	Remarks
High Temperature	240 HR , 70°C ± 2°C	No abnormalities in function
Operation	240 HK , 70 C ± 2 C	and appearance
Low Temperature	240 HR , -20°C ± 2°C	No abnormalities in function
Operation	240 HK , -20 C ± 2 C	and appearance
Thermal Shock Storage (NO operation state)	-30 °C (30 min.) → 25 °C (5 min.) → 80 °C (30 min.) → 25 °C (5 min.) 5 cycles	No abnormalities in function and appearance
Vibration (No operation state)	10 Hz ~ 55 Hz 0.3 mm / 1 Octave 55 Hz ~ 500 Hz 3g / 1 Octave 20 cycles per axis	No abnormalities in function and appearance

8.3 MTBF OF LIQUID CRYSTAL PANEL

50,000 hours, 90% Confidence Level at 25 °C and 65% RH Max.

If any of the following occurs after the MTBF test, the LCD is deemed to be failed:

- Current consumption increases three times the initial value.
- Damaged glass, plug and/or polarizer of the LCD.
- Non-operational display.

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9 OPERATING INSTRUCTIONS

9.1 I/O PIN FUNCTION (ARRAY LED B/L)

Pin No.	Function	Level	Description
1	Vss		Power Supply (0V,GND)
2	Vdd		Power Supply for Logic
3	Vo		Voltage Level for LCD Control Adjustment
4	RS	ı	Register Select H -> DB<0:7> : Instruction Register L -> DB<0:7> : Display RAM data
5	R/W	I	Read / Write signal
			H : Data Write (at falling of E when CS1B=L , CS2B=L and CS3=H)
			L : Data Read (when E=H , CS1B=L , CS2B=L and CS3=H)
6	E	I	Enable Signal
7 ~ 14	DB0 ~ 7	I/O	Display Data 0 ~ 7
15	/CS1	I	Chip select 1
16	/RST	I	Reset signal
17	Vee		Alternative Power Supply
18	/CS2	I	Chip select 2
19	N/A		No connection
20	N/A		No connection
BL1	LED(+)		Anode of LED B/L
BL2	LED(-)		Cathode of LED B/L

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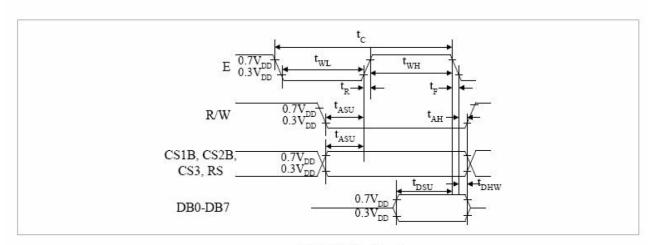


9.2 AC CHARACTERISTICS - INTERFACE TIMING DIAGRAM

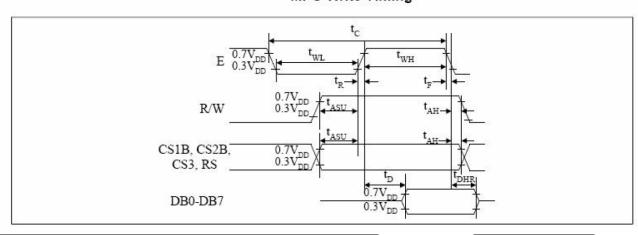
 $(Vdd = 5 V \pm 10\%, Ta = -30^{\circ}C \text{ to } +85^{\circ}C)$

MPU Interface

Characteristic	Symbol	Min	Type	Max	Unit
E cycle	tC	1000	1-	-:	
E high level width	tWH	450		-	ľ
E low level width	ťWL	450	- Th		1
E rise time	tR	(+	4	25	1
E fall time	tF	-	- 4	25	
Address set-up time	tASU	140	7-		ns
Address hold time	tAH	10	-	-	1
Data set-up time	tDSU	200		-	ř.
Data delay time	tD			320	×
Data hold time (write)	tDHW	10	1.5	7	1
Data hold time (read)	tDHR	20	~	- 2	1



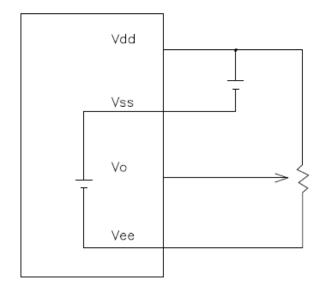
MPU Write Timing



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10 POWER SUPPLY

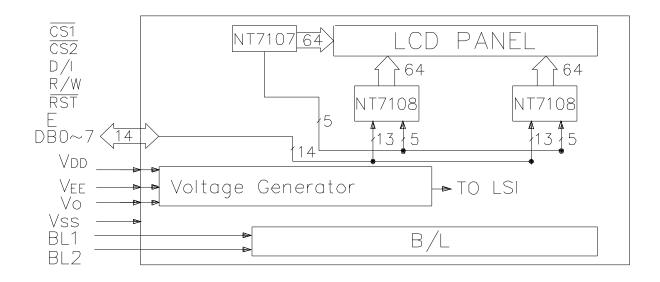


Negative Voltage built in

Note:

- 1. The Input Voltage for LCM (Vdd Vss) is 5 V.
- 2. The Recommended resistance of Vr is between 10k and 20k Ohms.

11 BLOCK DIAGRAM





12 LABELING DESCRIPTION

DENSITRON E R4328BG-SNY TAIWAN YYMM

13 PART NUMBER DESCRIPTION FOR AVAILABLE OPTIONS

LER4328①2128G64345

- Polarizer Type
 B = Transflective Positive Mode
- Backlight Color
 G = Yellow-Green
- Fluid Type and Temperature Range
 S = Standard temp. range; on-board Negative voltage supply generator
- Fluid Type and Temperature Compensation N = STN
- Background Color
 Y = Yellow mode STN

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14 QUALITY ASSURANCE SPECIFICATION

14.1 CONFORMITY

The performance, function and reliability of the shipped products conform to the Product Specification.

14.2 DELIVERY ASSURANCE

11.2.1 Delivery Inspection Standards

> IPC-AA610, Class 2 Electronic assemblies' standard.

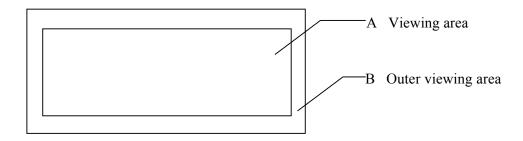
The Quality assurance levels are shown below:

Rank	Item Inspected	Defect type	AQL	Remark	
		No display			
		Over current			
		Missing segment			
	Display	Wrong Viewing direction		Fit/Function	
Major defect		Incorrect operation	0.25%	defect	
		No Backlight		defect	
		Flickering Backlight			
	Dimensions	PCB and/or Bezel out of			
	Dimensions	Specifications			
	LCD	Black and White spots			
		Black and White lines			
		Polarizer Scratches			
		Bubbles in Polarizer			
Minor defect		Segment deformations, Pin holes			
		Color Defect		Annagranga	
		Glass Chips	1.0%	Appearance defect	
williof defect		Wire Bonding Pad exposed		defect	
	COB	Insufficient covering with Resin			
	СОВ	(Wire Bonding line exposed)			
		Bubbles or Dust on COB			
	PCB	Dust or Solder balls on PCB			
	rCD	Pad Scratches			
	Tray	Particles]	Every Tray	
		Total	1.0%		

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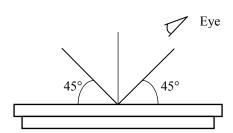


11.2.2 Zone Definition



11.2.3 Visual Inspection

- ❖ Inspect under 2 x 20 W or one 40 W fluorescent lamp (approximately 3000 lux.) leaving 25 to 30 cm between the module and the lamp and 30 cm between the module and the eye. (Measuring position).
- Appearance is inspected at the best contrast voltage (best contrast is adjusted by considering clarity and crosstalk on the screen).
- ❖ Inspect the module at 45° right and left, top and bottom.
- ❖ Use the optimum viewing angle during the contrast inspection.



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11.2.3.1 Standard of Appearance Inspection

Unit: mm

No.	Item			Criteria				Jnit: mm
1	Black spot, White spot,	Round type as shown: $\Phi = (X+Y)/2$						
	Dust	*			-	e quantity	I	
		Y		Size	_	Zone A	Zon	е В
		<u> </u>		$\Phi < 0.2$	A	ny number	Aı	nv
		X	<u> </u>	$0.2 < \Phi < 0.25$		2	nun	-
			0.	25 < Φ		0		
		Line type as shown:			1.1			
			41	Accepta	able g			7D
		Ler	ngth	Width	2.02	Zone A		Zone B
			- 2	$W \le 0$		Any num 2	ber	Any number
		L L	≥ 3	$0.03 < W \le 0$ 0.05 < W	0.03	As round	tuna	Humber
				0.03 < W		As Iouilu	туре	
		Total acceptable quantity	y: 5					
2	Polarizer Scratch	Scratch on Protective film is permitted. Scratch on Polarizer: Same as 1.						
3	Polarizer Bubble	$\Phi = (X+Y)/2$		Acc	entab	le quantity		
		-	-	Size		Zone A	Zone	e B
		M v	Ī	$\Phi < 0.2$		y number		
		<u> </u>	($0.2 < \Phi < 0.5$	•	3	An	y
		X		$0.5 < \Phi < 1.0$		1	num	
				1.0 < Ф		0		
		Total acceptable quanti	ty: 4					
4	Segment Deformation	1.a. Pin hole on segmen	ted dis	play:				
		W: Segment Width						
		$\Phi = (A+B)/2$		Accep	table	quantity		
				Width		Φ		
				W ≤ 0.4	Φ	\leq 0.2 and Φ	$0 \le \frac{1}{2}$	W
				W > 0.4		€ 0.25 and 4		
				acceptable quar les with Φ und				

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No.	Item		Criteria	
4	Segment Deformation	1.b. Pin hole on dot matrix display:		
	Deformation	\\\\	Acceptabl	e quantity
		<0.05	Size	-
			a, b < 0.1	Any number
			$(a+b)/2 \le 0.1$	Any number
			$0.5 < \Phi < 1.0$	3
		Total acceptable quantity: 7		
		2. Segments / dots with different w	idth:	
		a b		
			Acceptab	
			$a \ge b$	$a/b \le 4/3$
			a < b	a/b > 4/3
	Calan	3. Alignment layer defect: $\Phi = (a+b)/2$	Acceptabl Size $Φ \le 0.4$ $0.4 < Φ \le 1.0$ $1.0 < Φ \le 1.5$ $1.5 < Φ \le 2.0$	e quantity - Any number 5 3 2
5	Color Uniformity	Level of samples for approval is set	t as the limit.	
6	Backlight	The backlight color should correspond Flashing / flickering and / or non-fur Dust larger than 0.25 mm is not allo	nctioning backlight i	
7	СОВ	Exposed wire bonding pad is not all Insufficient covering with resin is no Dust or bubbles on the resin are not	ot allowed. (Exposed allowed.	
8	PCB	Non-melted solder paste should not Cold solder joints, missing solder co Residue or solder balls on the PCB a Short circuits on components are no	onnections, or oxidat are not allowed.	

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15 HANDLING PRECAUTIONS

Safety

If the LCD panel breaks, be careful not to get the liquid crystal fluid in your mouth or in your eyes. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

Mounting and Design

Place a transparent plate (e.g. acrylic, polycarbonate or glass) on the display surface to protect the display from external pressure. Leave a small gap between the transparent plate and the display surface. When assembling with a zebra connector, clean the surface of the pads with alcohol and keep the surrounding air very clean. Design the system so that no input signal is given unless the power supply voltage is applied.

Caution during LCD cleaning

Lightly wipe the display surface with a soft cloth soaked with Isopropyl alcohol, Ethyl alcohol or Trichlorotriflorothane. Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use aromatic solvents (toluene and xylene), or ketonic solvents (ketone and acetone).

Caution against static charge

As the display uses C-MOS LSI drivers, connect any unused input terminals to VDD or VSS. Do not input any signals before power is turned on. Also, ground your body, work / assembly table and assembly equipment to protect against static electricity.

Packaging

Displays use LCD elements, and must be treated as such. Avoid strong shock and drop from a height. To prevent displays from degradation, do not operate or store them exposed directly to sunlight or high temperature / humidity.

Caution during operation

It is indispensable to drive the display within the specified voltage limit since excessive voltage shortens its life. Direct current causes an electrochemical reaction with remarkable deterioration of the display quality. Give careful consideration to prevent direct current during ON/OFF timing and during operation. Response time is extremely delayed at temperatures lower than the operating temperature range while, at high temperatures, displays become dark. However, this phenomenon is reversible and does not mean a malfunction or a display that has been permanently damaged. If the display area is pushed on hard during operation, some graphics will be abnormally displayed but returns to a normal condition after turning off the display once. Even a small amount of condensation on the contact pads (terminals) can cause an electro-chemical reaction which causes missing rows and columns. Give careful attention to avoid condensation.

Storage

Store the display in a dark place where the temperature is $25^{\circ}\text{C} \pm 10^{\circ}\text{C}$ and the humidity below 50% RH. Store the display in a clean environment, free from dust, organic solvents and corrosive gases. Do not crash, shake or jolt the display (including accessories).

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