LIQUID CRYSTAL DISPLAY MODULE

Product Specification

CUSTOMER	**Standard**
CUSTOMER PART NUMBER	
PRODUCT NUMBER	**DET150XGNLNT0M-1A**

Product Mgr	Design Eng
Bruno Recaldini	**Sunny**
Date: 02-Jan-14	Date: 02-Jan-14

TABLE OF CONTENTS

REVISION RECORD

Rev.	Date	Page	Chapt.	Comment	ECN no.
1.0	02-Jan-14			Initial Release	

1 MAIN FEATURES

ITEM	CONTENTS
Screen Size	15.0” Diagonal
Display Format	1024 x RGB x 768 Dots
N° of Colour	16.7M
Overall Dimensions	326.5 mm (H) x 253.5 mm (V) x 12.0 mm (D)
Active Area	304.128 mm (H) x 228.096 mm (V)
LCD Type	TFT
Mode	MVA Transmissive / Normally White
Viewing Direction	Full view
Electrical Interface	LVDS
Backlight Type	LED
Operating Temperature	-20°C ~ +70°C
Storage Temperature	-30°C ~ +80°C
RoHS compliant	Yes

2 MECHANICAL SPECIFICATION

2.1 MECHANICAL CHARACTERISTICS

ITEM	CHARACTERISTIC	UNIT
Display Format	1024 x RGB x 768 Dots	Dots
Overall Dimensions	326.5 (H) x 253.5 (V) x 12.0 (D)	mm
Active Area	304.128 (H) x 228.096 (V)	mm
pixel Pitch	0.297 (H) x 0.297 (V)	mm
Weight	930 (MAX)	g

2.2 MECHANICAL DRAWING

Fig1: Reference outline drawing: Front side

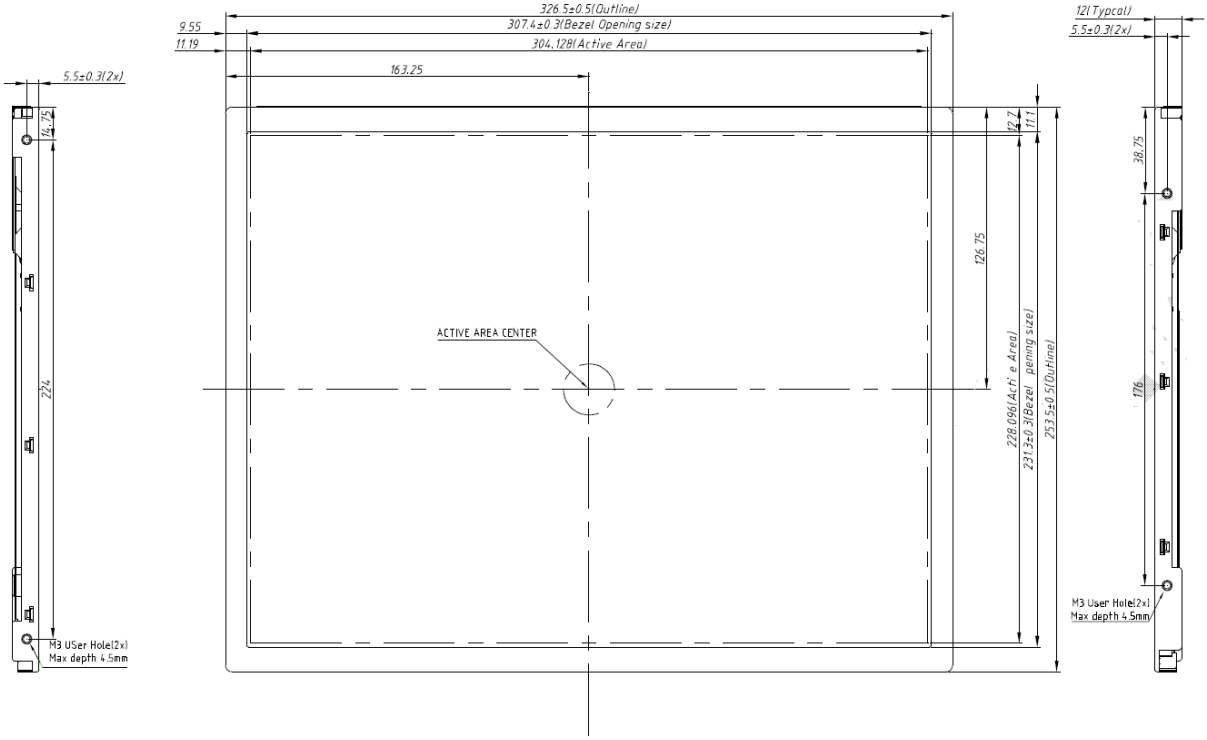
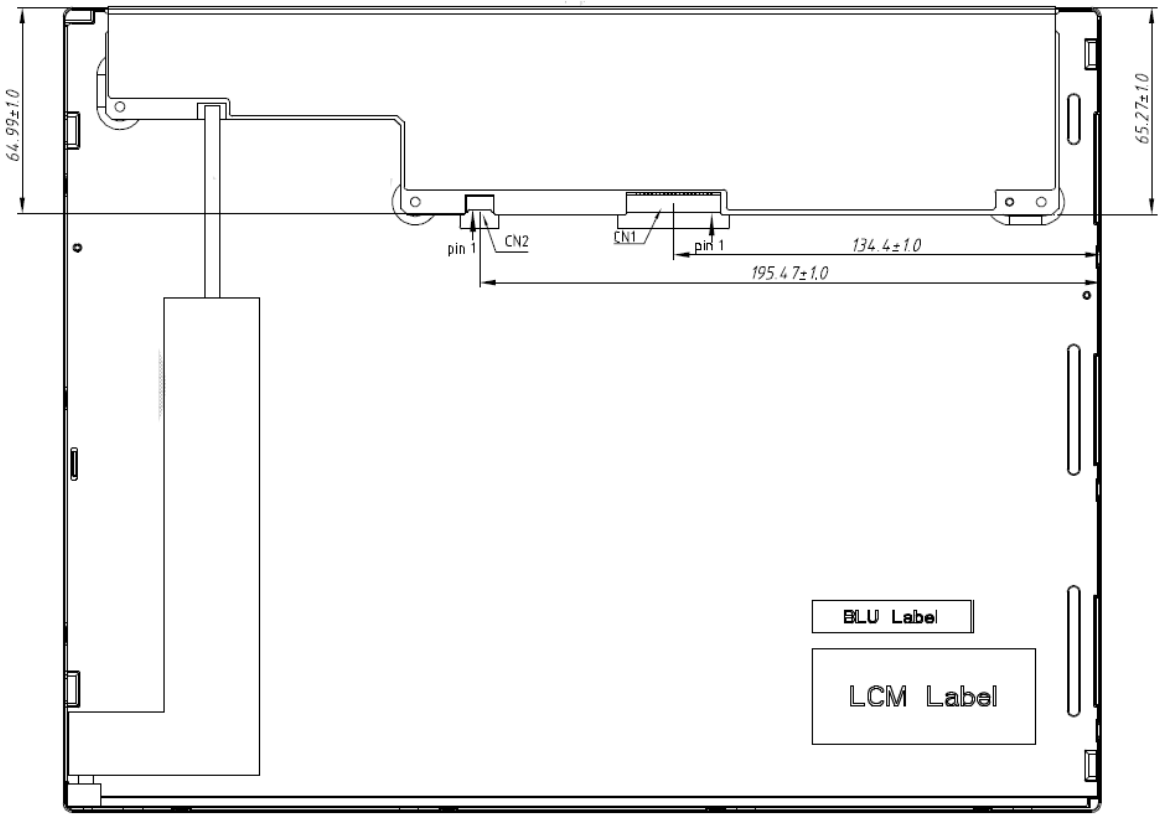


Fig2: Reference outline drawing: Back side



3 ELECTRICAL SPECIFICATION

3.1 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Condition	Min	Max	Unit	Note
Operating Temperature	TOP		-20	70	°C	1
Storage Temperature	TST		-30	80	°C	1,2,3
Operating Humidity	HOP		10	85	%RH	4
Storage Humidity	HST		10	95	%RH	4
Supply Voltage	VDD		-0.5	5	V	5

Please make sure to keep the temperature of LCD module is less than 80°C

Note 1. 90 % RH Max for Ta<50 °C, and 60% RH for Ta≥50°C.

Note 2. In case of below 0°C, the response time of liquid crystal (LC) becomes slower and the colour of panel becomes darker than normal one. Level of retardation depends on temperature, because of LC's characteristic.

Note 3. Only operation is guaranteed at operating temperature. Contrast, response time, another display quality are evaluated at +25°C.

Note 4. Storage Range & Operating Range Picture:

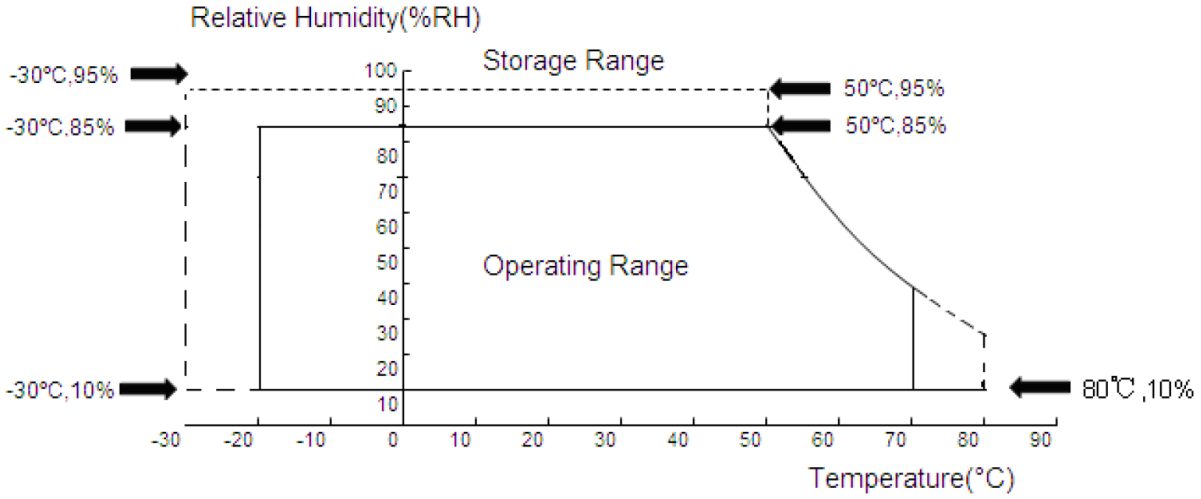


Fig 3: Storage Range & Operating Range Picture

Note 5. Humidity: 85%RH MAX (T<40°C) Note static electricity. Maximum wet bulb temperature at 39°C or less. (T>40°C) No condensation.

3.2 ELECTRICAL CHARACTERISTICS

Item		Symbol	Min.	Typ.	Max.	Units	Condition
LCD Drive Voltage (Logic)		VDD	3.0	3.3	3.6	V	-
VDD Current	Black Pattern	IDD	-	250	-	mA	3.3V/Black pattern
VDD Power Consumption	Black Pattern	PDD	-	-	1.3	W	Black Pattern, 60Hz
Rush Current		Irush	-	-	3	A	Note1
Allowable Logic/LCD Drive Ripple Voltage		VDDrp	-	-	200	[mV]p-p	Note2

Note1: Measure condition

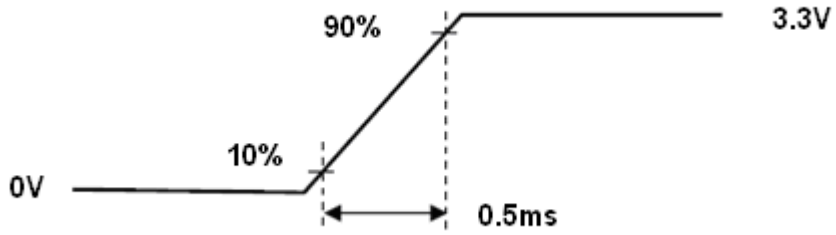


Fig 4: VDD Rising Time

Note 2: VDD Power Dip condition
If VTH<VDD ≤ Vmin, then td≤ 10ms; When the voltage returns to normal our panel must revive automatically.

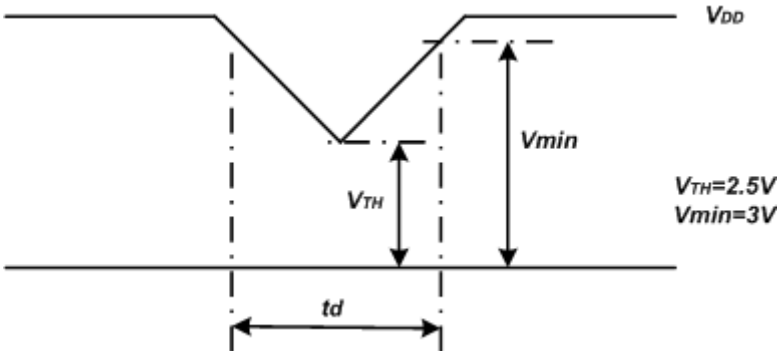


Fig 5: VDD Power Dip

If VTH<VDD≤ Vmin: When the voltage return to normal our panel must revive automatically.

3.3 INTERFACE PIN ASSIGNMENT

3.3.1 SIGNAL PIN ASSIGNMENT

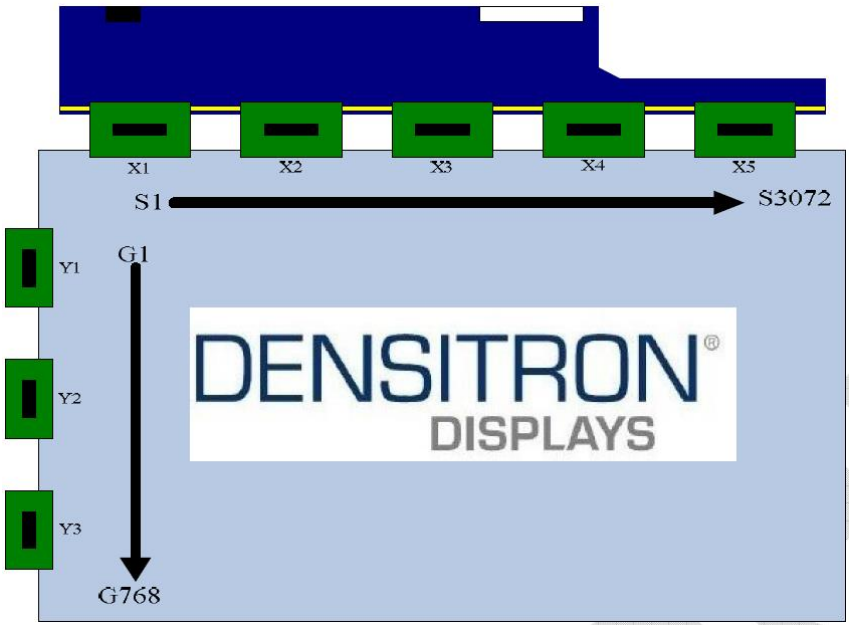
Item: FDP Down Connector (20 pin pitch=1.25mm)
Connector recommended model: MSB 240420HD

Pin #	Signal Name	Description	Remarks
1	VDD	LCD power supply (Typ. +3.3V)	
2	VDD	LCD power supply (Typ. +3.3V)	
3	VSS	Ground	
4	REV	Revers Scan Selection	Note
5	Rin1-	-LVDS differential data input (R0-R5,G0)	
6	Rin1+	+LVDS differential data input (R0-R5, G0)	
7	VSS	Ground	
8	Rin2-	-LVDS differential data input	
9	Rin2+	+LVDS differential data input	
10	VSS	Ground	
11	Rin3-	-LVDS differential data input	
12	Rin3+	+LVDS differential data input	
13	VSS	GND	
14	ClkIN-	-LVDS differential clock input	
15	ClkIN+	+LVDS differential clock input	
16	GND	GND	
17	Rin4-	-LVDS differential data input	
18	Rin4+	+LVDS differential data input	
19	VSS	Ground	
20	NC	Not connect	

Note1: REV=LOW/NC

Gate scan: Y1→Y2→Y3
Y1:G1→G2--→G258

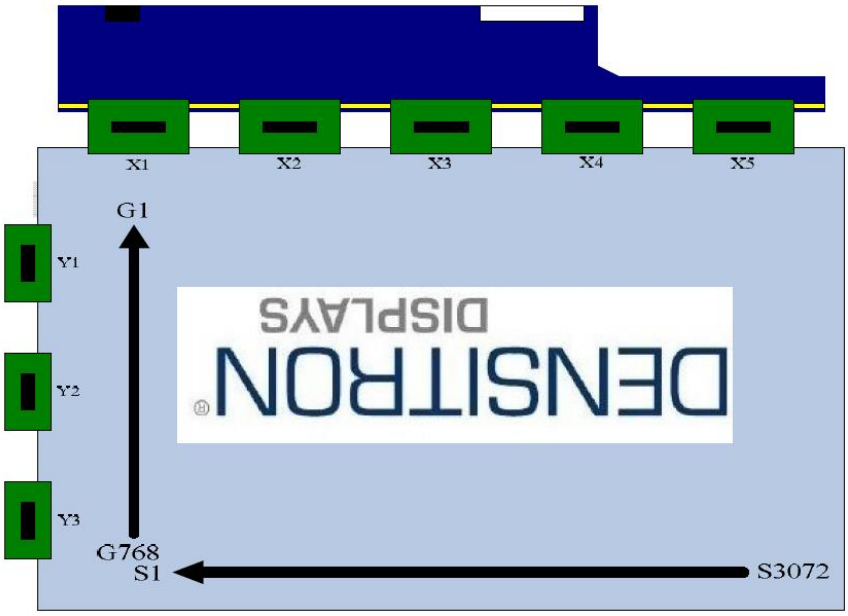
Source scans: X1→X2→X3→X4→X5
X1:S1→S2--→S600



Note2: REV= High

Gate scan: Y3→Y2→Y1 Source scans: X5→X4→X3→X2→X1

Y1:G258→G257--→G1 X1:S600→S599--→S1



3.4 TIMING CHARACTERISTICS

3.4.1 Interface timings

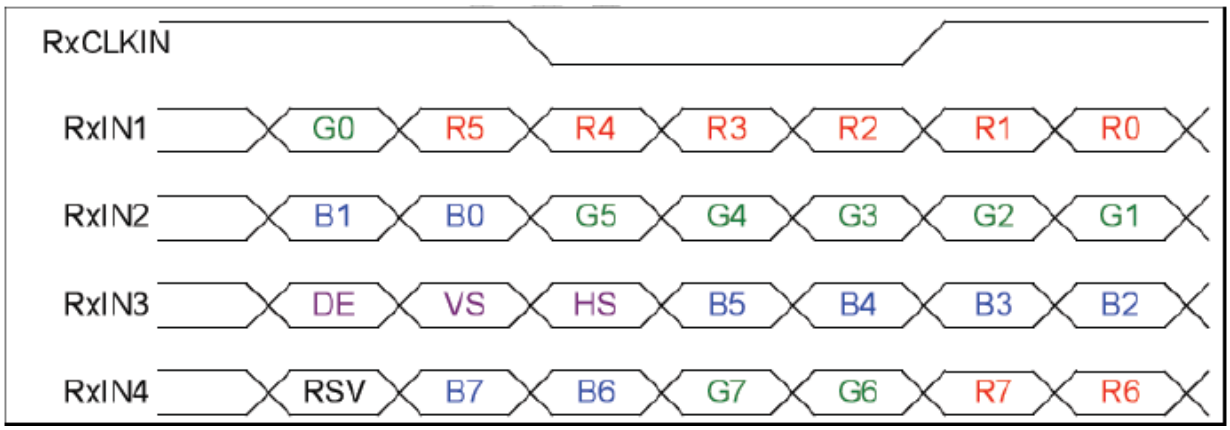
Synchronization Method: DE only

Parameter	Symbol	Unit	Min.	Typ.	Max.
LVDS Clock Frequency <single >	fdck	MHz	50	65	80
H Total Time	Thp	clocks	1056	1344	1720
H Active Time	HA	clocks	1024	1024	1024
H Front Porch	Thfp	clocks	-	48	-
H Sync Pulse Width	HSPW	clocks	-	32	-
H Back Porch	Thbp	clocks	-	240	-
H Frequency	fh	kHz	46.32	48.36	59.40
V Total Time	Tvp	lines	772	806	990
V Active Time	VA	lines	768	768	768
V Front Porch	Tvfp	lines	-	3	-
V Sync Pulse Width	VSPW	lines	-	12	-
V Back Porch	Tvbp	lines	-	23	-
V Frequency	fv	Hz	-	60	-

Note: H Blanking Time and V Blanking Time can not be changed at every frame

3.4.2 Timing Diagram of Interface Signal

Fig 6: Timing Characteristics



Note1: Follow SPWG
Note2: R/G/B data7: MSB, R/G/B data0: LSB

3.5 BLOCK DIAGRAM

It shows the functional block diagram of the LED module.

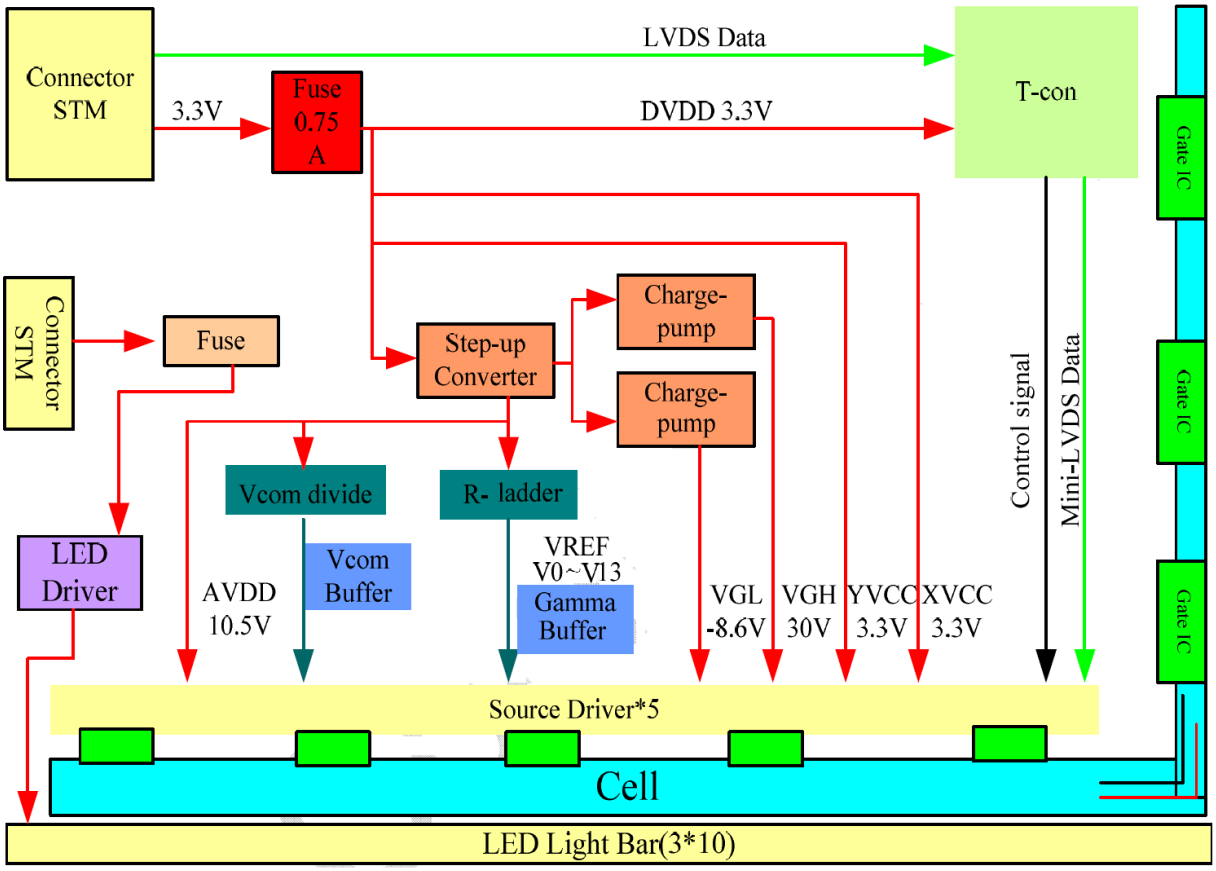


Fig 7: functional block diagram

3.6 PIXEL DATA FORMAT

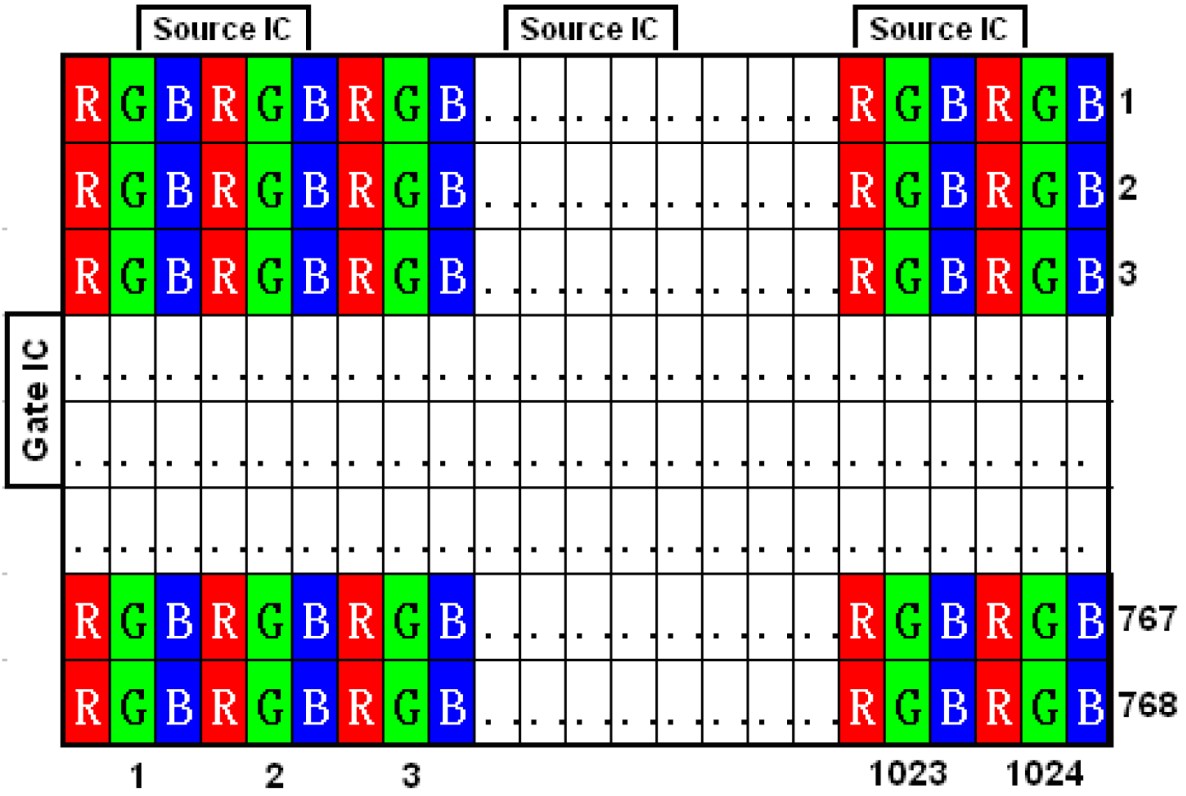


Fig 8: Pixel Format

3.7 POWER SEQUENCE

Power ON/OFF Sequence

VDD power on/off sequence is as follows. Signals from any system shall be Hi-resistance state or low level when VDD is off. Interface signals are also shown in the chart.

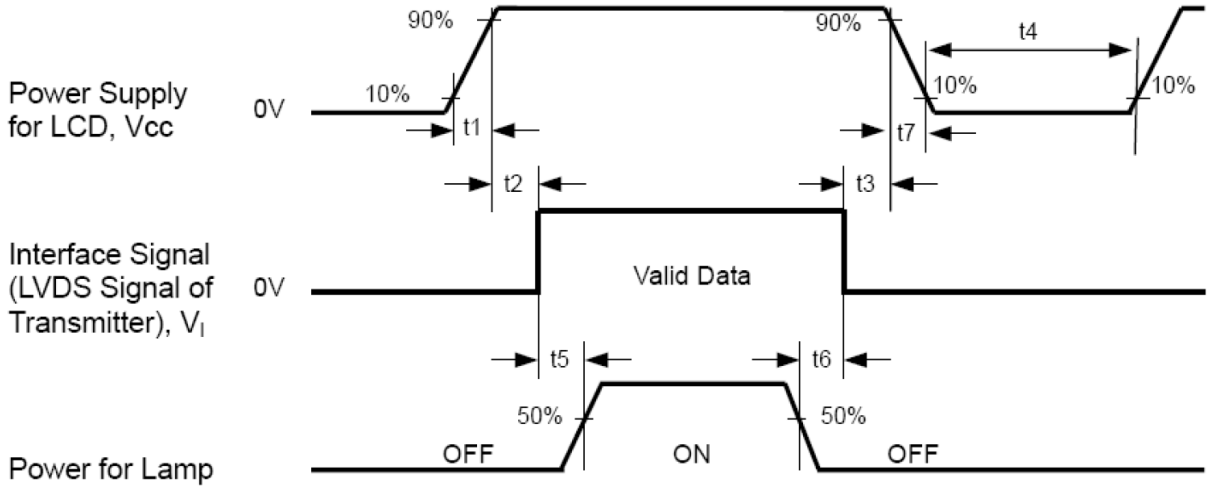


Fig 9: Power Sequencing

Table: Power ON/OFF sequence timing

Parameter	Symbol	Unit	min	Typ.	Max
VDD Rise Time	T1	ms	0.5	-	10
VDD Good to Signal Valid	T2	ms	0	-	20
Signal Disable to Power Down	T3	ms	0	-	1000
Power Off	T4	ms	1000	-	
Signal Valid to Backlight On	T5	ms	300	-	
Backlight Off to Signal Disable	T6	ms	200	-	
VDD Fall Time	T7	ms	0	-	100

4 OPTICAL SPECIFICATION

4.1 OPTICAL CHARACTERISTICS

Driving condition: IOVCC = VCI = 2.8V, VSS = 0V
Backlight: I_LED=240 mA, V_LED=12V, PWM_LED: Duty 100%
Measured temperature: Ta ＝ 25° C

Item		Symbol	Condition	MIN	TYP	MAX	Unit	Note
Response Time		TR+TF	θ=Φ=0° Normal Viewing Angle	-	16	-	ms	2
Contrast Ratio		CR		450	800	-		3
Viewing Angle	Left	θL	CR ≥ 10	70	80	-	deg	4
	Right	θR		70	80	-	deg	
	Up	ϕU		70	80	-	deg	
	Down	ϕD		70	80	-	deg	
Colour Chromaticity	Red	Rx	CR ≥ 10	Typ.-0.03	TBD	Typ.+0.03	-	5
		Ry			TBD		-	
	Green	Gx			TBD		-	
		Gy			TBD		-	
	Blue	Bx			TBD		-	
		By			TBD		-	
	White	Wx		0.255	0.305	0.355	-	
		Wy		0.275	0.325	0.375	-	
Centre Brightness			centre	TBD	500	-	cd/m²	6
Brightness Distribution			9 Points	75	80	-	%	7

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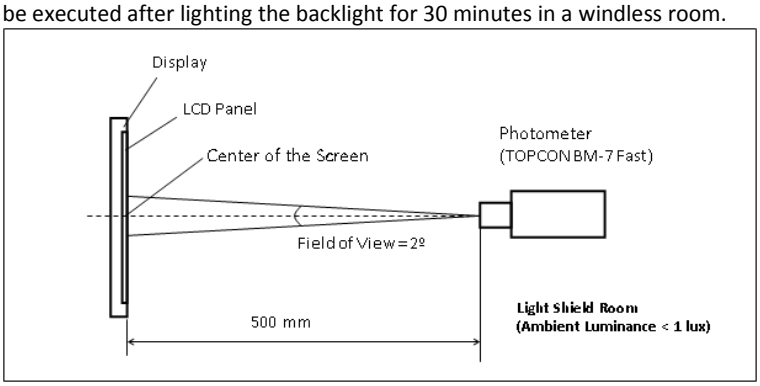
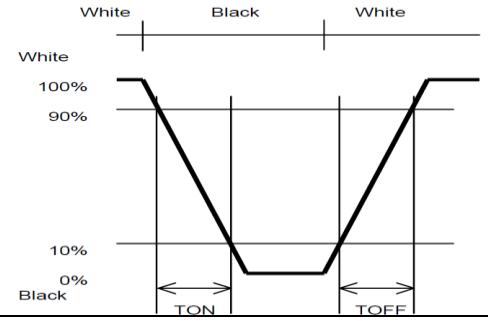
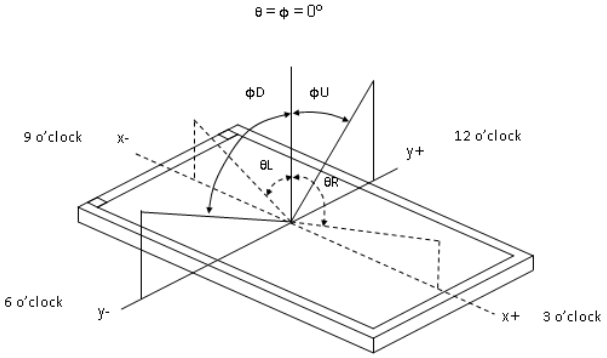
Test Method

Note	Item	Test method
1	Setup	The display should be stabilised at a given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilise the luminance, measurements should be executed after lighting the backlight for 30 minutes in a windless room. 
2	Response time	Measure output signal waveform by the luminance meter when raster of window pattern is changed from white to black and from black to white. 
3	Contrast ratio	Measure maximum brightness and minimum brightness at the centre of the screen by displaying raster or window pattern. Then calculate the ratio between these two values. Contrast Ratio (CR) = $\frac{\text{Brightness of unselected position (white)}}{\text{Brightness of selected position (black)}}$
4	Viewing angle Horizontal θ Vertical Ø	Move the luminance meter from right to left and up and down and determinate the angles where contrast ratio is 10 
5	Colour chromaticity	Measure chromaticity coordinates x and y of CIE1931 colorimetric system
6	Centre brightness	Measure the brightness at the centre of the screen
7	Brightness distribution	(Brightness distribution)= 100 x B/A % A: max. brightness of the 9 points B: min. brightness of the 9 points

5 BACKLIGHT SPECIFICATION

5.1 LED INTERFACE PIN ASSIGNMENT

Connector mode: MSB24038P5A or compatible
Manufactured: STM or compatible
Mating model number: P24038P5A or compatible

Pin#	Symbol	Signal Name
1	Vcc	12V
2	GND	GND
3	Enable	5V-On / 0V-Off
4	Dimming	PWM Dimming or Analog Dimming
5	NC	NC

5.2 PARAMETER GUIDELINE OF LED BACKLIGHT

Symbol	Parameter		Min.	Typ.	Max.	Units	Condition
VLED	LED Input		10.8	12	12.6	V	Ta=25°C
PLED	LED power consumption		-	-	(7.5)	W	
VLED_PWM	PWM signal voltage	High	4.5	5	5.5	V	
		Low	-	-	0.8		
FPWM	PWM dimming Frequency		200	-	20K	Hz	
VLED_EN	LED Enable voltage	High	2.0	5	5.5	V	
		Low	-	-	0.8		
LED Life Time	LED Life Time		(30,000)	-	-	Hours	Ta=25°C

Note1: The LED life time define as the estimated time to 50% degradation of initial luminous.
Note2: Operating temperature 25°C, humidity 55% RH
Note3: A higher LED power supply voltage will result in better power efficiency. Keep the V_LED between 12V and 12.6V is strongly recommended.

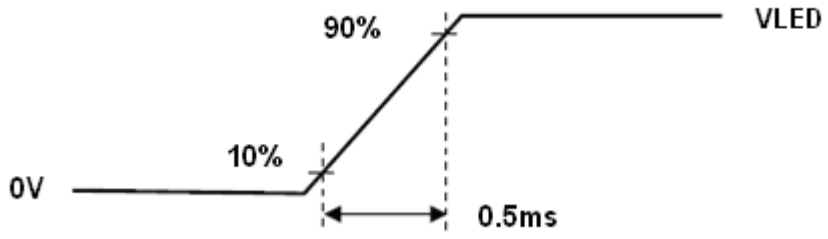


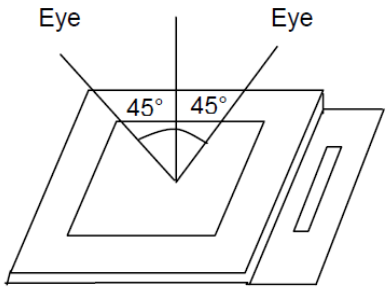
Fig10: LED Rush Current Measure Condition

6 QUALITY ASSURANCE SPECIFICATION

6.1 DELIVERY INSPECTION STANDARDS

Inspection Conditions

Inspection distance: 30 cm ± 2 cm
Viewing angle: ±45°



Environmental Conditions

Ambient temperature: 23°C ±5°C
Ambient humidity: 55±10% RH
Ambient illumination: 1000~1500 lux

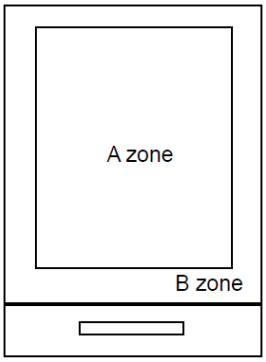
Sampling Conditions

1. Lot size: quantity of shipment lot per model
2. Sampling method:

Sampling Plan		ANSI / ASQC Z1.4-1993
		Normal inspection, Single Sampling
AQL	Major Defect	0.65%
	Minor Defect	1.5%

Definition of Area

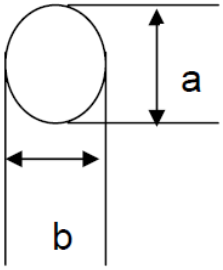
A zone: active area
B zone: viewing area



Basic Principle

A set of sample to indicate the limit of acceptable quality level shall be discussed should a dispute occur.

Inspection Criteria

<table>
<tr><th>No.</th><th>Item</th><th>Criteria (Unit: mm)</th></tr>
<tr><td>01</td><td>Black / White spot
Foreign material
(Round type)
Pinholes
Stain
Particles inside cell.
(Minor defect)</td><td>


φ= (a + b) /2
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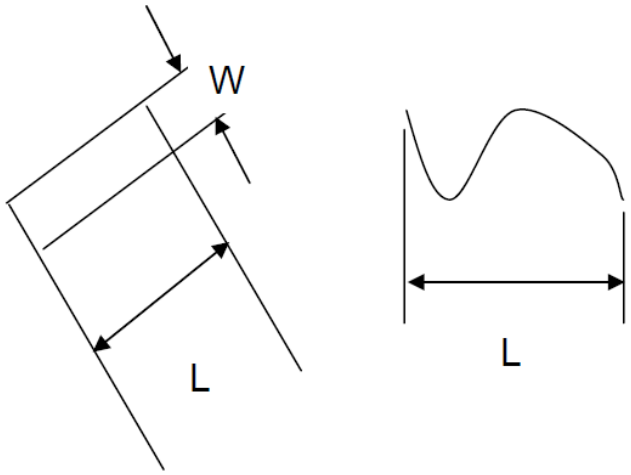
<tr><th>Area / Size</th><th>Acc. Qty</th></tr>
<tr><td>φ≤0.10</td><td>Ignore</td></tr>
<tr><td>0.10<φ≤0.15</td><td>2</td></tr>
<tr><td>0.15<φ≤0.25</td><td>1</td></tr>
<tr><td>0.25<φ</td><td>0</td></tr>
<tr><td>Total</td><td>2 no include
φ≤ 0.10</td></tr>
</table>
Distance between 2 defects should more than 3mm apart.
</td></tr>
<tr><td>02</td><td>Black and White line
Scratch
Foreign material
(Line type)
(Minor defect)</td><td>


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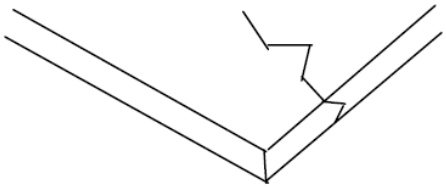
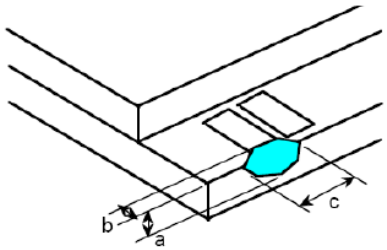
		<table><tr><th>Length</th><th>Width</th><th>Acc. Qty</th></tr><tr><td>/</td><td>W ≦ 0.03</td><td>Ignore</td></tr><tr><td>L ≦ 2.5</td><td>0.03 < W ≦ 0.05</td><td>3</td></tr><tr><td>L ≦ 2.5</td><td>0.05 < W ≦ 0.10</td><td>2</td></tr><tr><td>/</td><td>0.1 < W</td><td>0</td></tr><tr><td colspan="2">Total</td><td>3</td></tr></table> Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.
03	Glass Crack (Minor defect)	 Crack is potential to enlarge, any type is not allowed.
04	Glass Chipping Pad Area: (Minor defect) 	<table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td>c > 3.0, b< 1.0</td><td>1</td></tr><tr><td>c< 3.0, b< 1.0</td><td>3</td></tr><tr><td colspan="2">a<Glass Thickness</td></tr></table>

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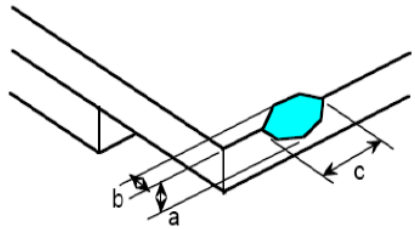
(Minor defect)
 
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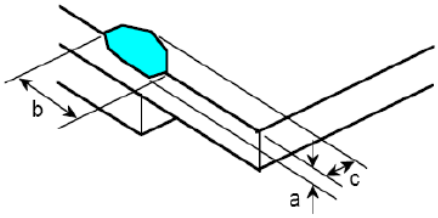
<td>Glass Chipping Except Pad Area:
(Minor defect)
 
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 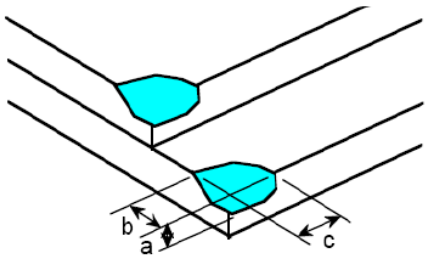
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 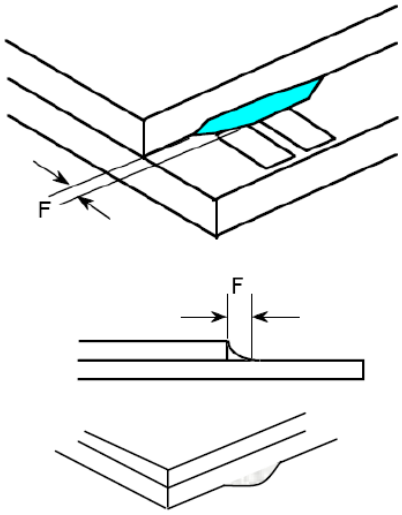 </td>
<td><table><tr><th>Length</th><th>Acc. Qty</th></tr><tr><td>F < 1.0</td><td>Ignore</td></tr></table>
Glass burr don’t affect assemble and module dimension.</td>
</tr>
<tr>
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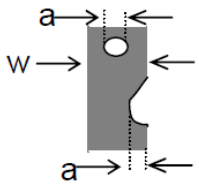
<td>FPC Defect:
(Minor defect)
  </td>
<td>9.1 Dent, pinhole width a<w/3.
(w: circuitry width.)
9.2 Open circuit is unacceptable.
9.3 No oxidation, contamination and distortion.</td>
</tr>
<tr>
<td>10</td>
<td>Bubble on Polarizer
(Minor defect)</td>
<td colspan="2"><table><tr><th>Diameter</th><th>Acc. Qty</th></tr><tr><td>φ≤0.20</td><td>Ignore</td></tr><tr><td>0.20 <φ≤0.30</td><td>4</td></tr><tr><td>0.30 < φ</td><td>None</td></tr></table></td>
</tr>
<tr>
<td>11</td>
<td>Dent on Polarizer
(Minor defect)</td>
<td colspan="2"><table><tr><th>Diameter</th><th>Acc. Qty</th></tr><tr><td>φ≤0.20</td><td>Ignore</td></tr><tr><td>0.20 <φ≤0.30</td><td>4</td></tr><tr><td>0.30 < φ</td><td>None</td></tr></table></td>
</tr>
<tr>
<td>12</td>
<td>Bezel</td>
<td colspan="2">12.1 No rust, distortion on the Bezel.
12.2 No visible fingerprints, stains or other contamination.</td>
</tr>
</table>

13	Touch Panel	D: Diameter W: width L: length 13.1 Spot: D<0.25 is acceptable 0.25≤D≤0.4 2dots are acceptable and the distance between defects should more than 10 mm. D>0.4 is unacceptable 13.2 Dent: D>0.40 is unacceptable 13.3 Scratch: W≤0.03, L≤10 is acceptable, 0.03<W≤0.10, L≤10 is acceptable Distance between 2 defects should more than 10 mm. W>0.10 is unacceptable.
14	PCB	14.1 No distortion or contamination on PCB terminals. 14.2 All components on PCB must same as documented on the BOM/component layout. 14.3 Follow IPC-A-600F.
15	Soldering	Follow IPC-A-610C standard
16	Electrical Defect (Major defect)	The below defects must be rejected. 16.1 Missing vertical / horizontal segment, 16.2 Abnormal Display. 16.3 No function or no display. 16.4 Current exceeds product specifications. 16.5 LCD viewing angle defect. 16.6 No Backlight. 16.7 Dark Backlight. 16.8 Touch Panel no function. 16.9 Dark Dot –one Allowed. 16.10 Bright Dot – one Allowed. Remark: 1. A pixel defect is acceptable if one color is none functional and causes a bright dot. The display may have one case where one color is out and cause a dark dot. 2. Bright dot caused by scratch and foreign object accords to item 1.

Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

Classification of Defects

Visual defects (except no or wrong label) are treated as minor defects, while electrical defects are treated as major defects.
Two minor defects are equal to one major defect in lot sampling inspection.

Identification / marking criteria

Any unit with illegible / wrong / double or no marking / label shall be rejected.

6.2 DEALING WITH CUSTOMER COMPLAINTS

Non-conforming analysis

Purchaser should supply Densitron with detailed data of non-conforming sample.
After accepting it, Densitron should complete the analysis in two weeks from receiving the sample.
If the analysis cannot be completed on time, Densitron must inform the purchaser.

Handling of non-conforming displays

If any non-conforming displays are found during customer acceptance inspection which Densitron is clearly responsible for, return them to Densitron.
Both Densitron and customer should analyse the reason and discuss the handling of non-conforming displays when the reason is not clear.
Equally, both sides should discuss and come to agreement for issues pertaining to modification of Densitron quality assurance standard.

7 RELIABILITY SPECIFICATION

7.1 RELIABILITY TESTS

Test Item		Test Condition
Durability Test	High Temperature Storage	Ta= 80˚C 96h
	Low Temperature Storage	Ta=-30˚C 96h
	Temperature Cycle Storage	-20°C for 30 min, then 70°C for 30 min, 20 cycles
	High Temperature Operation	Tp= 70°C 96h
	Low Temperature Operation	Tp= -20°C 96h
	High Temperature & Humidity Operation	Tp= 40°C RH= 90% 96h Non condensing
	Thermal Shock Resistance	The sample should be allowed to stand the following 5 cycles of operation: TSTL for 30 minutes -> normal temperature for 5 minutes -> TSTH for 30 minutes -> normal temperature for 5 minutes, as one cycle, then taking it out and drying it at normal temperature, and allowing it stand for 24 hours

Note: Ta=ambient temperature Tp= Panel temperature

Notes:
1. No dew condensation to be observed.
2. The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.
3. No cosmetic or functional defects should be allowed.
4. Total current consumption should be less than twice the initial value.

8 HANDLING PRECAUTIONS

Safety
If the LCD panel breaks, be careful not to get the liquid crystal fluid in your mouth or in your eyes. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

Mounting and Design
Place a transparent plate (e.g. acrylic, polycarbonate or glass) on the display surface to protect the display from external pressure. Leave a small gap between the transparent plate and the display surface.
When assembling with a zebra connector, clean the surface of the pads with alcohol and keep the surrounding air very clean.
Design the system so that no input signal is given unless the power supply voltage is applied.

Caution during LCD cleaning
Lightly wipe the display surface with a soft cloth soaked with Isopropyl alcohol, Ethyl alcohol or Trichlorotriflorothane.
Do not wipe the display surface with dry or hard materials that will damage the polariser surface.
Do not use aromatic solvents (toluene and xylene), or ketonic solvents (ketone and acetone).

Caution against static charge
As the display uses C-MOS LSI drivers, connect any unused input terminal to VDD or VSS. Do not input any signals before power is turned on. Also, ground your body, work/assembly table and assembly equipment to protect against static electricity.

Packaging
Displays use LCD elements, and must be treated as such. Avoid strong shock and drop from a height. To prevent displays from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity.

Caution during operation
It is indispensable to drive the display within the specified voltage limit since excessive voltage shortens its life. Direct current causes an electrochemical reaction with remarkable deterioration of the display quality. Give careful consideration to prevent direct current during ON/OFF timing and during operation. Response time is extremely delayed at temperatures lower than the operating temperature range while, at high temperatures, displays become dark. However, this phenomenon is reversible and does not mean a malfunction or a display that has been permanently damaged. If the display area is pushed on hard during operation, some graphics will be abnormally displayed but returns to a normal condition after turning off the display once. Even a small amount of condensation on the contact pads (terminals) can cause an electro-chemical reaction which causes missing rows and columns. Give careful attention to avoid condensation.

Storage
Store the display in a dark place where the temperature is 25°C ± 10°C and the humidity below 50%RH.Store the display in a clean environment, free from dust, organic solvents and corrosive gases.
Do not crash, shake or jolt the display (including accessories).