DENSITRON® DISPLAYS

LIQUID CRYSTAL DISPLAY MODULE

Product Specification

PRODUCT NUMBER	LR4220

INTERNAL APPROVALS		
Product Manager	Engineering	Document Control
Date:	Date:	Date:

FORM No. DT-029 V2

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REVISION RECORD

Rev.	Date	Page	Par.	Comment	ECN no.
A	06/04/09	--	--	Initial DCA Release	E4098

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1 GENERAL SPECIFICATIONS

ITEM	DESCRIPTION	UNIT
Module Outline Dimensions	80.0 (W) x 36.0 (H) x 15.0 Max (D)	mm
Viewing Area	64.5 (W) x 13.8 (H)	mm
Active Display Area	59.45 (W) x 5.95 (H)	mm
Configuration Format	16 Characters (W) x 1 Lines (H)	--
Character Dimensions	3.20 (W) x 5.95 (H)	mm
Character Pitch	3.75 (W) x 5.95 (H)	mm
LCD Type	STN / Transflective / Positive	--
Backlight Type	Array LED / Yellow-Green	--
Duty Ratio	1/16	--
Bias Drive	1/4	--
Controller / Interface	Sitronix ST7066 / 8-bit Parallel interface	--
Power Supply	Vdd (+5)	V
RoHS Complaint	Yes	--

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2 MECHANICAL DRAWING

UNIT: mm
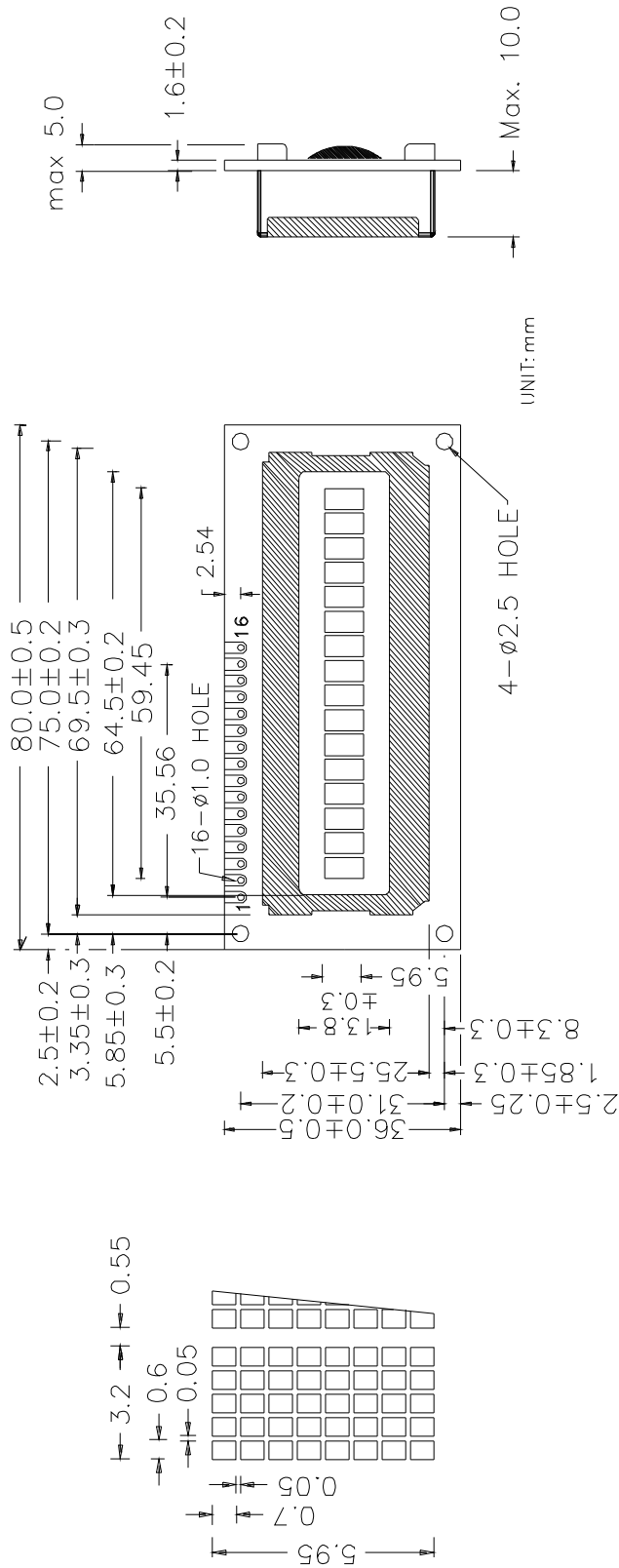
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3 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Conditions	Min.	Max.	Unit
Power Supply Voltage	Vdd	Ta = 25°C, 50 ± 10% RH	0	7.0	V
Operating Temperature	Topr	< 65% RH (Wide Temp.)	-20	70	°C
Storage Temperature	Tstg	< 65% RH (Wide Temp.)	-30	80	°C
		< 48 hrs	20	90	% RH
		< 1000 hrs	20	65	% RH

4 ELECTRONIC CHARACTERISTICS

4.1 DC CHARACTERISTICS

(Vss = 0 V, Ta = 25°C)

Item		Symbol	Conditions	Min.	Typ.	Max.	Unit
Operating Voltage		Vdd		4.75	--	5.25	V
Input Voltage	High	Vihc	--	0.7Vdd	--	Vdd	V
	Low	Vilc		0	--	0.55	
LCD Driving Voltage		Vdd - Vo		3.0	--	10.0	V

4.2 LCD CURRENT CONSUMPTION & DRIVING VOLTAGE

(Vdd – Vss = 5.0 V)

		STN TEMPERATURE	
		Normal Temp.	Wide Temp.
Supply Current, (Idd) Typ., mA		N/A	1.0
Supply Current, (Iee) Typ., mA		N/A	N/A
Recommended LCD Driving voltage			
LCD Driving Voltage (Vdd – Vo)	Ta = -20 °C	N/A	4.3
	Ta = 0 °C	N/A	4.2
	Ta = 25 °C	N/A	4.1
	Ta = 50 °C	N/A	4.0
	Ta = 70 °C	N/A	4.3

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5 OPTOELECTRONIC CHARACTERISTICS

(Ta = 25°C)

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	Note
Viewing Angle	θ1 (down)	CR ≥ 2	--	40	--	deg.	5.1
	θ2 (up)	CR ≥ 2	--	10	--	deg.	5.1
	θ3 (right)	CR ≥ 2	--	30	--	deg.	5.2
	θ4 (left)	CR ≥ 2	--	30	--	deg.	5.2
Contrast Ratio	CR	Ta = 25°C	--	10	15	--	5.3
Response Time	Tr	Ta = 25°C	--	250	750	ms	5.4
	Tf	Ta = 25°C	--	300	900		

Note 5.1: Definition of Viewing angle, θ1 & θ2   Note 5.2: Definition of Viewing angle, θ3 & θ4

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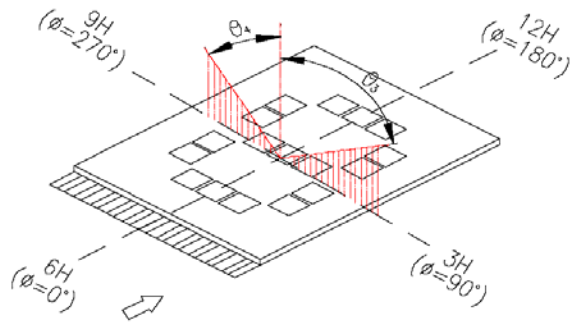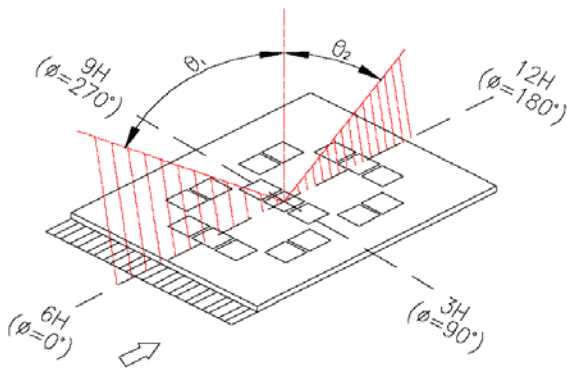
Note 5.3: Definition of Contrast Ratio (CR)

Intensity

[positive type]

$Cr = Loff / Lon$

[Negative type]

$Cr = Lon / Loff$
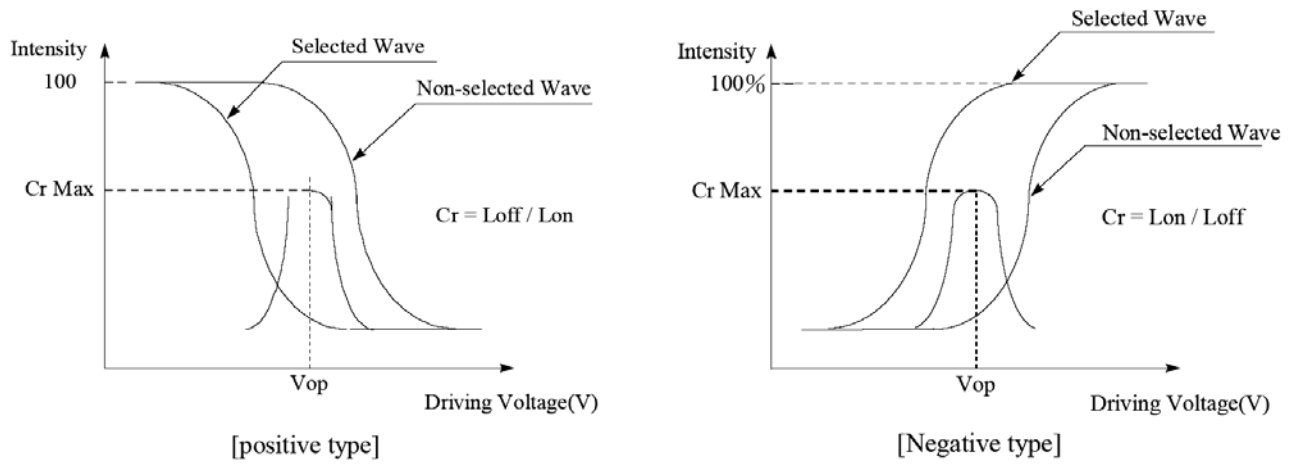
Note 5.4: Definition of Response Time

[positive type]

[Negative type]
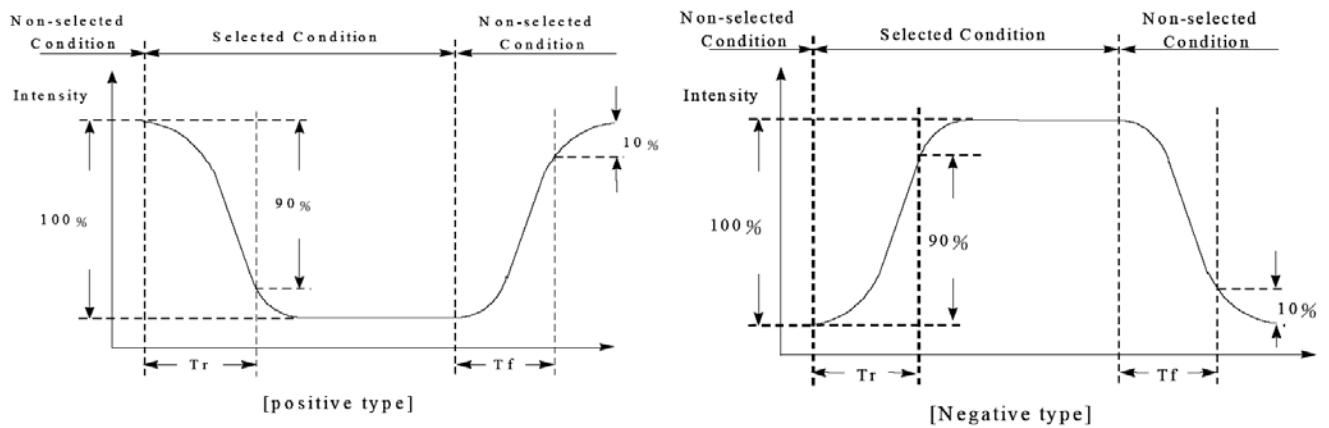
6 ARRAY LED BACKLIGHT ELECTRICAL CHARACTERISTICS

Item	Conditions	Standard			Unit
		Min.	Typ.	Max.	
Input voltage	Ta = 25 °C	---	5.0	---	V(DC)
Current consumption	Ta = 25 °C		120		mA
Average brightness (B/L only) (Ta = 25 °C, IL = 120 mA)	Test when connecting after 3 min. Ta = 25 C (max. contrast)				cd/m2 (Note 3)
	Yellow-green array B/L	---	200	---	
Brightness uniformity	Ta = 25 °C , IL = 120 mA	80	---	---	% (Note 4)
Lamp life	Ta = 25 °C , IL = 120 mA Humidity : 30%RH ~ 85%RH	---	50,000	---	Hrs (Note 5)
Operating Temp.	Humidity : 30%RH ~ 85%RH	-20	---	70	°C
Storage Temp.	Humidity : 30%RH ~ 85%RH	-30	---	80	°C
limit resistor (R1)	Ta = 25 °C	---	6.8	---	OHM (Note 2)

Note 2 : Built-in BL current limit resistor on LCDM.

$N = 12$, $I = 10$ mA
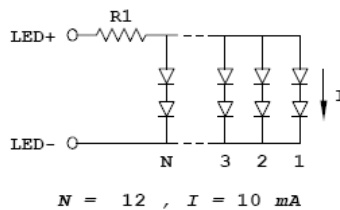
3 : Average brightness of 3 points when B/L is used at the beginning.
4 : Brightness uniformity = (MIN / MAX) x 100 %
5 : Half of the original average brightness
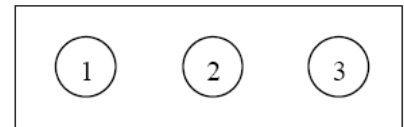
7 RELIABILITY TEST

7.1 RELIABILITY CHARACTERISTICS (WIDE TEMP.)

Test Item	Test Condition	Remarks
High Temperature Operation	240 HR , 70°C ± 2°C	No abnormalities in function and appearance
Low Temperature Operation	240 HR , -20°C ± 2°C	No abnormalities in function and appearance
Thermal Shock Storage (NO operation state)	-30 °C (30 min.) → 25 °C (5 min.) → 80 °C (30 min.) → 25 °C (5 min.) 5 cycles	No abnormalities in function and appearance
Vibration (No operation state)	10 Hz ~ 55 Hz 0.3 mm / 1 Octave 55 Hz ~ 500 Hz 3g / 1 Octave 20 cycles per axis	No abnormalities in function and appearance

7.2 MTBF OF LIQUID CRYSTAL PANEL

50,000 hours, 90% Confidence Level at 25 °C and 65% RH Max.

If any of the following occurs after the MTBF test, the LCD is deemed to be failed:

- Current consumption increases three times the initial value.
- Damaged glass, plug and/or polarizer of the LCD.
- Non-operational display.

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8 OPERATING INSTRUCTIONS

8.1 I/O PIN FUNCTION (ARRAY LED B/L)

Pin No.	Function	Level	Description
1	Vss/ LED(-)	-	Ground (0V) / Cathode of LED B/L
2	Vdd	-	Logic Supply Voltage (+5V)
3	Vo	-	Voltage Level for LCD Control Adjustment
4	RS	I	Register Select 0: Instruction Register 1: Data Register
5	R/W	I	Read / Write 0: Data Write (Module-MPU) 1: Data Read (Module-MPU)
6	E	I	Enable Signal Active High (H - L)
7 ~ 14	DB0 ~ 7	I/O	Bi-directional data bus line 0 ~ 7
15	LED(+)		Anode of LED B/L
16	LED(-)		Cathode of LED B/L

ST7066U

■ AC Characteristics

(TA = 25℃ , VCC = 5V)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
		Internal Clock Operation				
f_{OSC}	OSC Frequency	R = 91KΩ	190	270	350	KHz
		External Clock Operation				
f_{EX}	External Frequency	-	125	270	410	KHz
	Duty Cycle	-	45	50	55	%
T_R, T_F	Rise/Fall Time	-	-	-	0.2	μs
		Write Mode (Writing data from MPU to ST7066U)				
T_C	Enable Cycle Time	Pin E	1200	-	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R, T_F	Enable Rise/Fall Time	Pin E	-	-	25	ns
T_{AS}	Address Setup Time	Pins: RS,RW,E	0	-	-	ns
T_{AH}	Address Hold Time	Pins: RS,RW,E	10	-	-	ns
T_{DSW}	Data-Setup Time	Pins: DB0 - DB7	40	-	-	ns
T_H	Data Hold Time	Pins: DB0 - DB7	10	-	-	ns
		Read Mode (Reading Data from ST7065U to MPU)				
T_C	Enable Cycle Time	Pin E	1200	-	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R, T_F	Enable Rise/Fall Time	Pin E	-	-	25	ns
T_{AS}	Address Setup Time	Pins: RS,RW,E	0	-	-	ns
T_{AH}	Address Hold Time	Pins: RS,RW,E	10	-	-	ns
T_{DDR}	Data Setup Time	Pins: DB0 - DB7	-	-	100	ns
T_H	Data Hold Time	Pins: DB0 - DB7	10	-	-	ns
		Interface Mode with LCD Driver(ST7065)				
T_{CWH}	Clock Pulse with High	Pins: CL1, CL2	800	-	-	ns
T_{CWL}	Clock Pulse with Low	Pins: CL1, CL2	800	-	-	ns
T_{CST}	Clock Setup Time	Pins: CL1, CL2	500	-	-	ns
T_{SU}	Data Setup Time	Pin: D	300	-	-	ns
T_{DH}	Data Hold Time	Pin: D	300	-	-	ns
T_{DM}	M Delay Time	Pin: M	0	-	2000	ns

■ Timing Characteristics

● Writing data from MPU to ST7066U
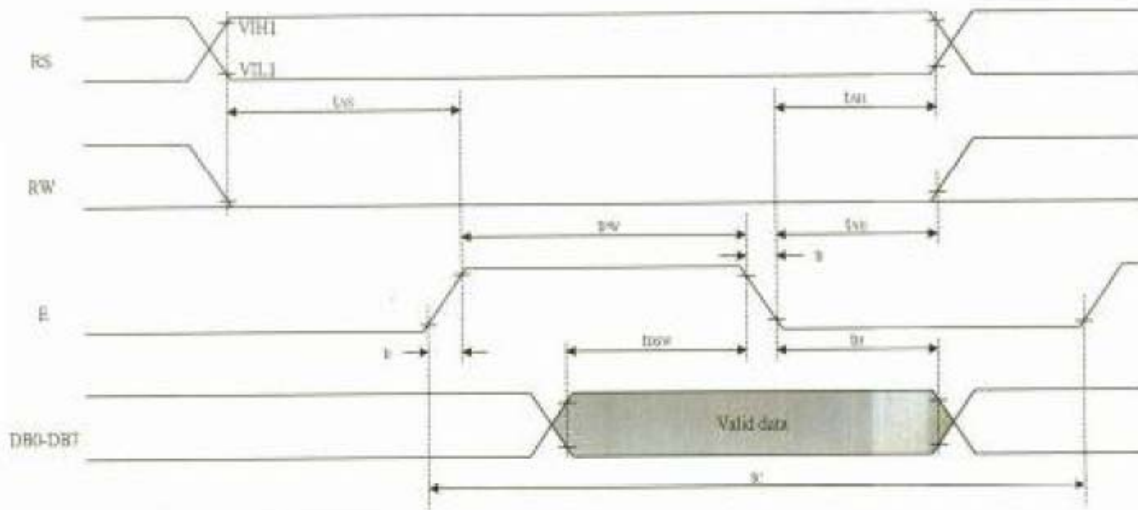
● Reading data from ST7066U to MPU

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NO.7066-0A

Character code table / font pattern chart

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9 LABELING DESCRIPTION

9.1 Gray Mode

DENSITRONLR
4220BG-HNG
TAIWAN YYMM

9.2 Yellow Mode

DENSITRONLR
4220BG-HNY
TAIWAN YYMM

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10 POWER SUPPLY
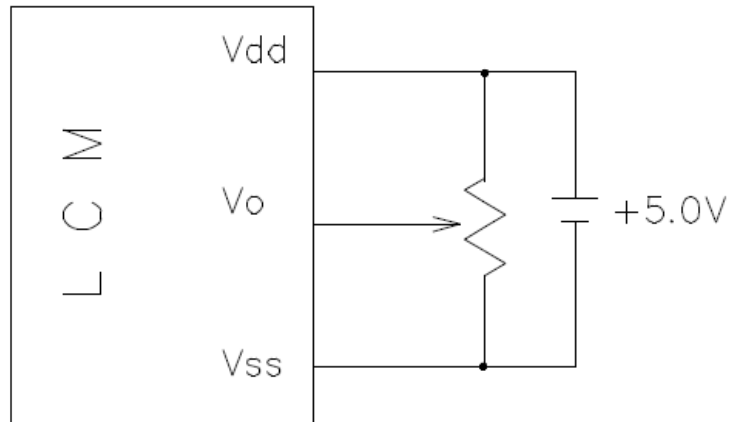
RECOMMENDED VR : 10K ohm ~ 20K ohm

11 BLOCK DIAGRAM

FORM No. DT-029 V2
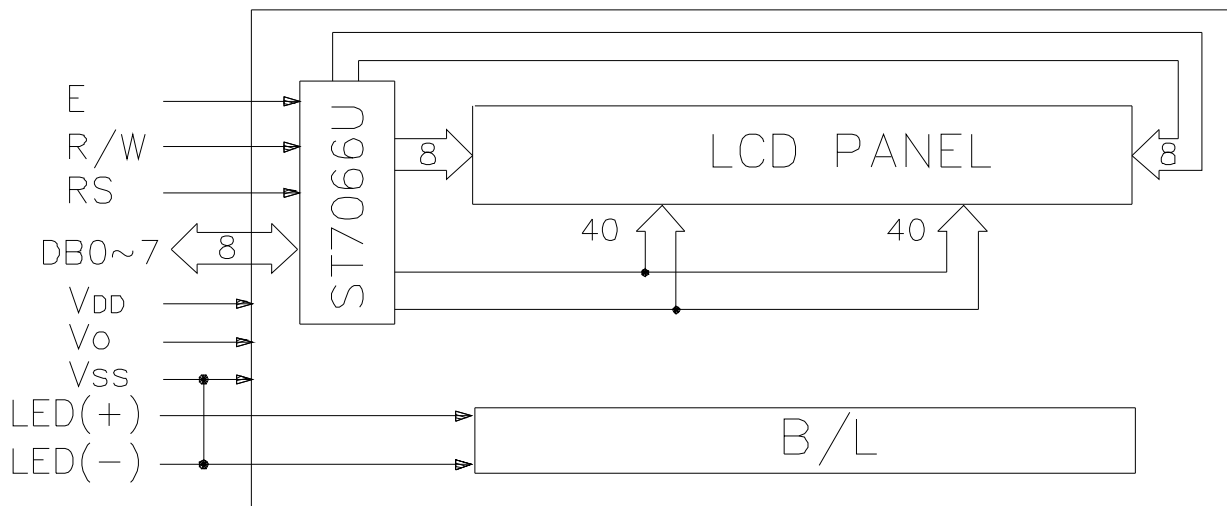
12 PART NUMBER DESCRIPTION FOR AVAILABLE OPTIONS

LR4220①②1C16③④⑤

①
    Polarizer Type
    B = Transflective Positive Mode

②
    Backlight Color
    G = Yellow-Green

③
    Fluid Type and Temperature Range
    H = Wide temp. range; negative supply voltage required

④
    Fluid Type and Temperature Compensation
    N = STN

⑤
    Background Color
    Y = Yellow mode STN
    G = Gray mode STN

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13 QUALITY ASSURANCE SPECIFICATION

13.1 CONFORMITY

The performance, function and reliability of the shipped products conform to the Product Specification.

13.2 DELIVERY ASSURANCE

13.2.1 Delivery Inspection Standards

➢ IPC-AA610, Class 2 Electronic assemblies' standard.

The Quality assurance levels are shown below:

Rank	Item Inspected	Defect type	AQL	Remark
Major defect	Display	No display	0.25%	Fit/Function defect
		Over current		
		Missing segment		
		Wrong Viewing direction		
		Incorrect operation		
		No Backlight		
		Flickering Backlight		
	Dimensions	PCB and/or Bezel out of Specifications		
Minor defect	LCD	Black and White spots	1.0%	Appearance defect
		Black and White lines		
		Polarizer Scratches		
		Bubbles in Polarizer		
		Segment deformations, Pin holes		
		Color Defect		
		Glass Chips		
	COB	Wire Bonding Pad exposed		
		Insufficient covering with Resin (Wire Bonding line exposed)		
		Bubbles or Dust on COB		
	PCB	Dust or Solder balls on PCB		
		Pad Scratches		
	Tray	Particles		Every Tray
		Total	1.0%	

13.2.2 Zone Definition

A   Viewing area
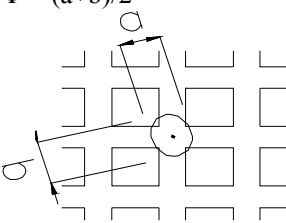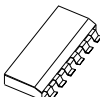
B   Outer viewing area
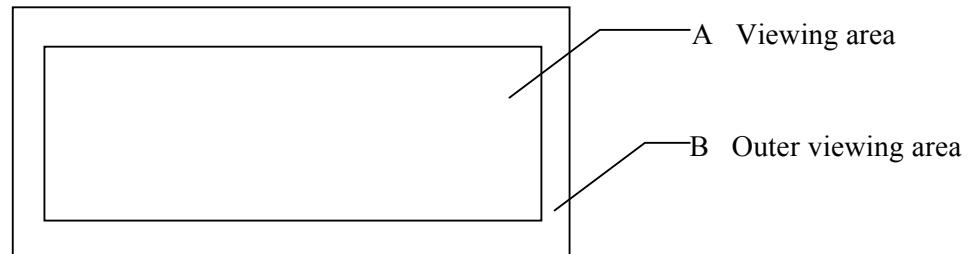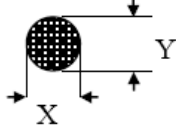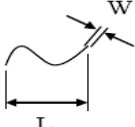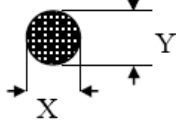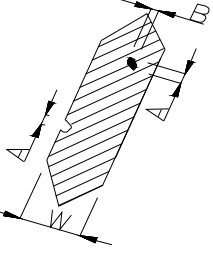
13.2.3 Visual Inspection
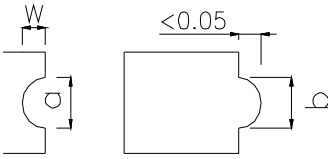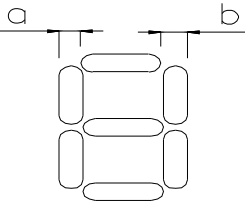
- ❖ Inspect under 2 x 20 W or one 40 W fluorescent lamp (approximately 3000 lux.) leaving 25 to 30 cm between the module and the lamp and 30 cm between the module and the eye. (Measuring position).
- ❖ Appearance is inspected at the best contrast voltage (best contrast is adjusted by considering clarity and crosstalk on the screen).
- ❖ Inspect the module at 45° right and left, top and bottom.
- ❖ Use the optimum viewing angle during the contrast inspection.

Eye

45°   45°

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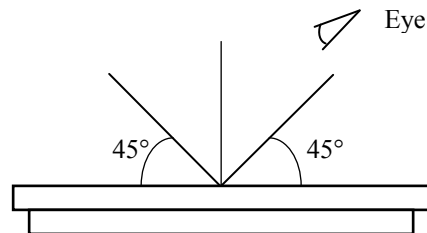
13.2.3.1 Standard of Appearance Inspection

Unit: mm

No.	Item	Criteria
1	Black spot, White spot, Dust	Round type as shown: $\Phi = (X+Y)/2$ **Acceptable quantity**<table><tr><td>Size</td><td>Zone A</td><td rowspan="4">Zone B</td></tr><tr><td>$\Phi < 0.2$</td><td>Any number</td></tr><tr><td>$0.2 < \Phi < 0.25$</td><td>2</td></tr><tr><td>$0.25 < \Phi$</td><td>0</td></tr></table>Zone B: Any number Line type as shown: **Acceptable quantity**<table><tr><td>Length</td><td>Width</td><td>Zone A</td><td>Zone B</td></tr><tr><td>-</td><td>$W \le 0.03$</td><td>Any number</td><td rowspan="3">Any number</td></tr><tr><td>$L \le 3$</td><td>$0.03 < W \le 0.05$</td><td>2</td></tr><tr><td>-</td><td>$0.05 < W$</td><td>As round type</td></tr></table> Total acceptable quantity: 5
2	Polarizer Scratch	Scratch on Protective film is permitted. Scratch on Polarizer: Same as 1.
3	Polarizer Bubble	$\Phi = (X+Y)/2$ **Acceptable quantity**<table><tr><td>Size</td><td>Zone A</td><td rowspan="5">Zone B Any number</td></tr><tr><td>$\Phi < 0.2$</td><td>Any number</td></tr><tr><td>$0.2 < \Phi < 0.5$</td><td>3</td></tr><tr><td>$0.5 < \Phi < 1.0$</td><td>1</td></tr><tr><td>$1.0 < \Phi$</td><td>0</td></tr></table> Total acceptable quantity: 4
4	Segment Deformation	1.a. Pin hole on segmented display: W: Segment Width $\Phi = (A+B)/2$ **Acceptable quantity**<table><tr><td>Width</td><td>Φ</td></tr><tr><td>$W \le 0.4$</td><td>$\Phi \le 0.2$ and $\Phi \le \frac{1}{2}W$</td></tr><tr><td>$W > 0.4$</td><td>$\Phi \le 0.25$ and $\Phi \le (1/3)W$</td></tr></table>Total acceptable quantity: 1 Defect per segment. Pin holes with Φ under 0.10 mm are acceptable.

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No.	Item	Criteria
4	Segment Deformation	1.b. Pin hole on dot matrix display:

Acceptable quantity	
Size	-
a, b < 0.1	Any number
(a+b)/2 ≤ 0.1	Any number
0.5 < Φ <1.0	3

Total acceptable quantity: 7

2. Segments / dots with different width:

Acceptable limits	
a ≥ b	a/b ≤ 4/3
a < b	a/b > 4/3

3. Alignment layer defect:

Φ = (a+b)/2

Acceptable quantity	
Size	-
Φ ≤ 0.4	Any number
0.4 < Φ ≤ 1.0	5
1.0 < Φ ≤ 1.5	3
1.5 < Φ ≤ 2.0	2

No.	Item	Criteria
5	Color Uniformity	Level of samples for approval is set as the limit.
6	Backlight	The backlight color should correspond to the product specification. Flashing / flickering and / or non-functioning backlight is not allowed. Dust larger than 0.25 mm is not allowed.
7	COB	Exposed wire bonding pad is not allowed. Insufficient covering with resin is not allowed. (Exposed Wire bonding line) Dust or bubbles on the resin are not allowed.
8	PCB	Non-melted solder paste should not be present on the PCB. Cold solder joints, missing solder connections, or oxidation is not allowed. Residue or solder balls on the PCB are not allowed. Short circuits on components are not allowed.

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14 HANDLING PRECAUTIONS

Safety
If the LCD panel breaks, be careful not to get the liquid crystal fluid in your mouth or in your eyes.
If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

Mounting and Design
Place a transparent plate (e.g. acrylic, polycarbonate or glass) on the display surface to protect the display from external pressure. Leave a small gap between the transparent plate and the display surface.
When assembling with a zebra connector, clean the surface of the pads with alcohol and keep the surrounding air very clean. Design the system so that no input signal is given unless the power supply voltage is applied.

Caution during LCD cleaning
Lightly wipe the display surface with a soft cloth soaked with Isopropyl alcohol, Ethyl alcohol or Trichlorotrifluoroethane. Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use aromatic solvents (toluene and xylene), or ketonic solvents (ketone and acetone).

Caution against static charge
As the display uses C-MOS LSI drivers, connect any unused input terminals to VDD or VSS. Do not input any signals before power is turned on. Also, ground your body, work / assembly table and assembly equipment to protect against static electricity.

Packaging
Displays use LCD elements, and must be treated as such. Avoid strong shock and drop from a height.
To prevent displays from degradation, do not operate or store them exposed directly to sunlight or high temperature / humidity.

Caution during operation
It is indispensable to drive the display within the specified voltage limit since excessive voltage shortens its life. Direct current causes an electrochemical reaction with remarkable deterioration of the display quality. Give careful consideration to prevent direct current during ON/OFF timing and during operation.
Response time is extremely delayed at temperatures lower than the operating temperature range while, at high temperatures, displays become dark. However, this phenomenon is reversible and does not mean a malfunction or a display that has been permanently damaged. If the display area is pushed on hard during operation, some graphics will be abnormally displayed but returns to a normal condition after turning off the display once. Even a small amount of condensation on the contact pads (terminals) can cause an electro-chemical reaction which causes missing rows and columns. Give careful attention to avoid condensation.

Storage
Store the display in a dark place where the temperature is 25°C ± 10°C and the humidity below 50% RH.
Store the display in a clean environment, free from dust, organic solvents and corrosive gases.
Do not crash, shake or jolt the display (including accessories).