

LIQUID CRYSTAL DISPLAY MODULE

Product Specification

CUSTOMER	Standard	
PRODUCT NUMBER	LMR4755	
CUSTOMER APPROVAL		Date 22 June 2006

INTERNAL APPROVALS				
Product Mgr Doc Control Electr. Eng				
Bruno Anthony Recaldini Perkins		_		
		Luo Luo		

☐ Ap	prova	for	Specif	ication	only
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 $[\]hfill\square$ Approval for Specification and Sample



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REVISION RECORD

Rev.	Date	Page	Chapt.	Comment	ECR no.
A	07/03/04			Product release	
В	10/04/06	9	3	New Controller chip	
С	21/02/07	16	6	Modified brightness figure	
D	20/04/12	4	1	Change the driver IC to NT7086	
Е	24/05/12	16	6	 Change to brighter LED B/L The current consumption is changed to 160mA (5V) 	

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1 MAIN FEATURES

ITEM	CONTENTS	
Display Format	320 x 240 dots	
Overall Dimensions	165.5 x 109 x 14	
Viewing Area	122 x 92	
LCD type	STN Blue mode or FSTN	
Mode	Transflective or Transmissive	
Viewing Angle	6 O clock	
Duty ratio	1 / 240	
Driver IC Neotec NT7086		
Backlight type	LED	
Backlight colour White		
DC/DC converter	Optional	
Operating temperature $-20 \sim +70^{\circ}\text{C}$		
Storage temperature	-30 ~ +80°C	
RoHS status Compliant		

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2 MECHANICAL SPECIFICATION

2.1 MECHANICAL CHARACTERISTICS

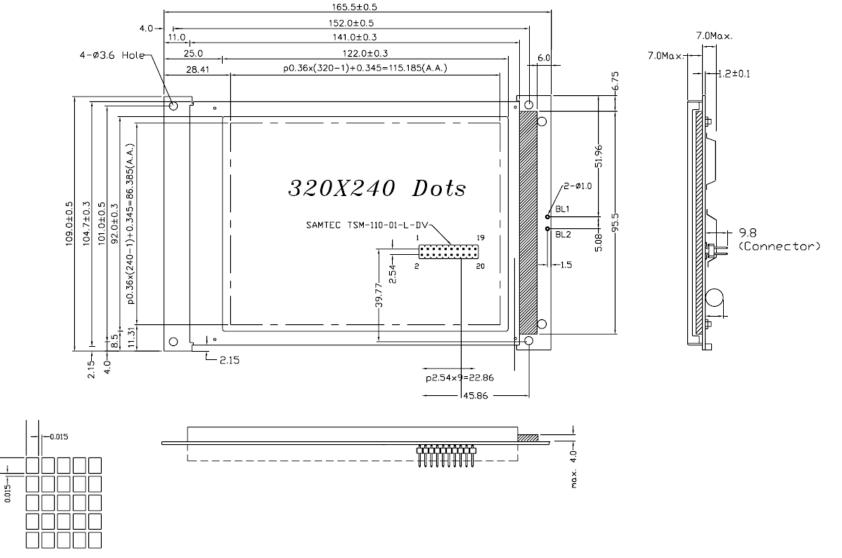
ITEM	CHARACTERISTIC	UNIT
Display Format	320 x 240 dots	
Overall Dimensions	165.5 x 109 x 14	mm
Viewing Area	122 x 92	mm
Active Area	115.185 x 86.385	mm
Dot Size	0.345 x 0.345	mm
Dot Pitch	0.360 x 0.360	mm
IC Controller/Driver	NT7086	

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2.2 MECHANICAL DRAWING



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3 ELECTRICAL SPECIFICATION

3.1 ABSOLUTE MAXIMUM RATINGS

VSS = 0 V, Ta = 25 °C

Item	Symbol	Min	Max	Unit	Note	
Power Supply Voltage	V_{DD}	0	7	V		
Input Voltage	Vin	0	V_{DD}	V		
Operating Temperature	Topr	-20	+70	°C	Note 1	
Storage Temperature	Tst	-30	+80	°C	Note 2	
Static Electricity	Be sure that you are grounded when handling displays.					

Note 1: Background colour changes slightly depending on ambient temperature. This phenomenon is reversible. Ta≤70 °C: 75% RH max

Note 2: Ta≤80 °C: 75% RH max

3.2 ELECTRICAL CHARACTERISTICS

VSS = 0 V, Ta = 25 °C

Item	Symbol	Condition	Min	Тур	Max	Unit
Input Voltage	V _{ILc}	V _{DD} =	3.8		V_{DD}	V
Input Voltage	V_{IHc}	5.0±5%	0		1	V
LCD Module Driving Voltage	Vop	Ta = 25 °C	22.2		22.8	V
Current Consumption	* I _{DD}	Single +5v operation		85		mA
Current Consumption	*Idd			10		mA
Current Consumption	IEE			6		mA
Supply Voltage	VEE			-22		V

^{*} I_{DD} measurement condition is for all pattern ON



3.3 INTERFACE PIN ASSIGNMENT

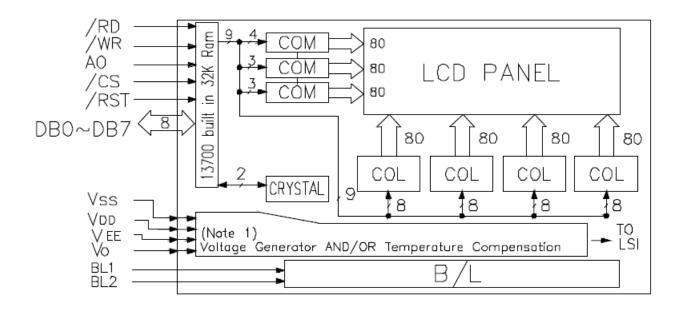
No.	Symbol	Function
1	V_{SS}	Ground
2	Vdd	Power supply for logic
3	V0	Voltage level for LCD Control adjustment
4	/RD	Generic host bus interface when the signal is active low
5	/WR	Generic host bus interface when the signal is active low
6	A0	Indirect addressing mode
7	DB0	Display data
8	DB1	Display data
9	DB2	Display data
10	DB3	Display data
11	DB4	Display data
12	DB5	Display data
13	DB6	Display data
14	DB7	Display data
15	/CS	Chip select
16	/RST	Reset signal
17	Vee	Alternative power supply
18	N/C	No connection
19	N/C	No connection
20	N/C	No connection
BL1	LED+	Anode for LED backlight
BL2	LED-	Cathode for LED backlight

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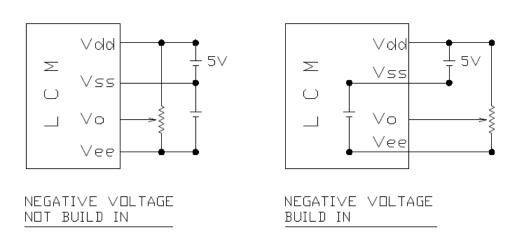
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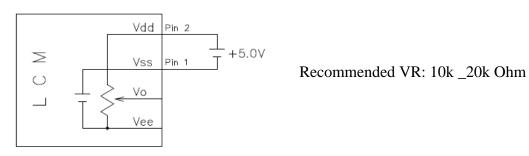


3.4 BLOCK DIAGRAM



3.5 POWER SUPPLY CIRCUIT





NEGATIVE VOLTAGE & TEMPERATURE COMPENSATION BUILT IN

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3.6 CHARACTER GENERATOR ROM MAP

The following figure shows the character codes and the codes allocated to CGRAM. All codes can be used by the CGRAM if not using the internal ROM, but the CGRAM address must be set to 0.

Note

If either of CGRAM1 or CGRAM2 are used, only 1 bpp is supported.

	Upper 4 bits															
Lower 4 bits	0	1	2	3	4	5	6	7	8	9	Α	В	С	D	Ε	F
0				0	@	Р	•	р				_	J	Ξ		
1			į	1	Α	Q	а	q			0	7	チ	4		
2			"	2	В	R	b	r			Γ	1	IJ	لر		
3			#	3	С	S	С	s			1	þ	Ţ	E		
4			\$	4	D	Т	d	t			`	İ	ł	t		
5			%	5	Е	U	е	u				7	t	ı		
6			&	6	F	٧	f	٧			F	Ħ	<u>-</u>	∃		
7				7	G	W	g	w			7	†	Z	ラ		
8			(8	Н	Х	h	х			1	7	才	リ		
9)	9	ı	Υ	i	у			ク	生	J	ıμ		
А			*	:	J	Z	j	z			Н		IJ	V		
В			+	;	K	[k	{			7	ij	t			
С			,	<	L	¥	ı	:			4	:)	J	7		
D				=	М]	m	}			4	ス	^	ン		
Е			-	>	N	۸	n	\rightarrow			=	セ	市	',		
F			/	?	0	_	0	+			≘	9	マ	•		
CGRAM1 CGRAM2 CGRAM2																

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4 TIMING CHARECTERISTICS

4.1 Input Clock

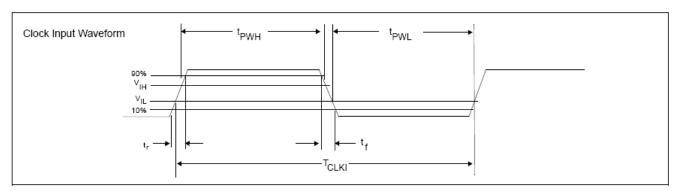


Figure 7-1 Clock Input Requirements

Table 7-1 Clock Input Requirements

Symbol	Parameter	3.0V		5.0V		Units
	i didilietei	Min	Max	Min	Max	Office
f _{CLKI}	Input Clock Frequency (CLKI)	_	60		60	MHz
T _{CLKI}	Input Clock period (CLKI)	1/f _{osc}	_	1/f _{osc}	_	ns
t _{PWH}	Input Clock Pulse Width High (CLKI)	0.4T _{CLKI}	_	0.4T _{CLKI}	_	ns
t _{PWL}	Input Clock Pulse Width Low (CLKI)	0.4T _{CLKI}	_	0.4T _{CLKI}		ns
t _f	Input Clock Fall Time (10% - 90%)	_	2	_	2	ns
t _r	Input Clock Rise Time (10% - 90%)	_	2	_	2	ns

Note

Maximum internal requirements for clocks derived from CLKI must be considered when determining the frequency of CLKI. For further details on internal clocks, see Section 9, "Clocks" on page 41.

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4.2 Reset Timing

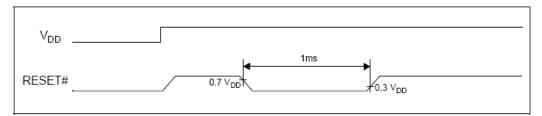


Figure 7-2 Reset Timing

The S1D13700F01 requires a reset pulse of at least 1 ms after power-on in order to reinitialize its internal state. For maximum reliability, it is not recommended to apply a DC voltage to the LCD panel while the S1D13700F01 is reset. Turn off the LCD power supplies for at least one frame period after the start of the reset pulse.

During the reset period the S1D13700F01 cannot receive commands. Commands to initialize the internal registers should be issued soon after a reset. During reset, the LCD drive signals FPDAT, FPLINE and FR are halted.

A delay of 3 ms (maximum) is required following the rising edges of both RESET# and VDD to allow for system stabilization. This delay allows the clock used by the internal oscillator circuit to become stable before use.

Symbol	Parameter	3.3 Volt		5.0 Volt		Units
Syllibol	i didilietei		Max	Min	Max	Units
t1	CS# setup time	5	_	5	_	ns
t2	AB[15:0] setup time	5	_	5	_	ns
t3	WR#, RD# falling edge to WAIT# driven low	2	15	2	15	ns
t4	DB[7:0] setup time to WR# rising edge (write cycle)	Note 2	_	Note 2	_	ns
t5	RD# falling edge to DB[7:0] driven (read cycle)	3	_	3	_	ns
t6	CS# hold time	7	_	7	_	ns
t7	AB[15:0] hold time	7	_	7	_	ns
t8	RD#, WR# rising edge to WAIT# high impedance	2	10	2	10	ns
t9	DB[7:0] hold time from WR# rising edge (write cycle)	5	_	5	_	ns
t10	DB[7:0] hold time from RD# rising edge (read cycle)	3	14	3	14	ns
t11	WAIT# rising edge to valid Data	_	Note 3	_	Note 3	ns
t12	RD#, WR# pulse inactive time	Note 4	_	Note 4	_	ns
t13	WAIT# pulse active time	_	Note 5		Note 5	ns

Table 7-2 Generic Bus Direct/Indirect Interface with WAIT# Timing

- 1. Ts = System clock period
- 2. t4min = 2Ts + 5
- 3. t11max = 1Ts + 5 (for 3.3V)
 - = 1Ts + 7 (for 5.0V)
- 4. t12min = 1Ts (for a read cycle followed by a read or write cycle)
 - = 2Ts + 2 (for a write cycle followed by a write cycle)
 - = 5Ts + 2 (for a write cycle followed by a read cycle)
- 5. t13max = 4Ts + 2

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5 OPTICAL SPECIFICATION

5.1 OPTICAL CHARACTERISTICS

5.1.1 STN Negative

Ta = 25 °C

Item	Symbol	Condition	Min	Тур	Max	Unit	Note	
	θ1	CR≥2		45		deg	1	
Viewing Angle	θ2	CR≥2		40		deg	1	
Viewing Angle	θ3	CR≥2		35		deg	2	
	θ4	CR≥2		35		deg	2	
Contrast Ratio	CR	Ta = 25 °C		7.33	7.41	-	3	
Desmanas Tima	Tr	Ta = 25 °C		48.2	300	ms	4	
Response Time	Tf	Ta = 25 °C		98.7	300			
Duissing Mathod	Duty			1/240				
Driving Method	Bias	as 1/13						
LCD Type	STN – Negative							
Viewing Direction	1 6 O'CLOCK							

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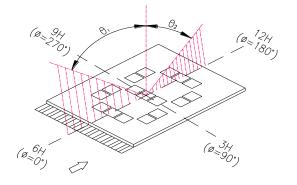
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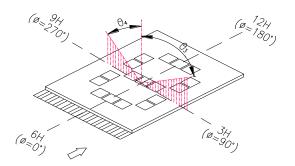


5.1.2 FSTN Positive

Item	Symbol	Condition	Min	Тур	Max	Unit	Note	
	θ1	CR≥2		30		deg	1	
Viewing Angle	θ2	CR≥2		25		deg	1	
Viewing Angle	θ3	CR≥2		25		deg	2	
	θ4	CR≥2		25		deg	2	
Contrast Ratio	CR	Ta = 25 °C	4.5	6.5	8.5	-	3	
Dasmonso Timo	Tr	Ta = 25 °C	200	250	380	ms	4	
Response Time	Tf	Ta = 25 °C	120	150	230			
Driving Mathod	Duty	Outy 1/240						
Driving Method	Bias	Bias 1/13						
LCD Type	FSTN-Positive / Transflective							
Viewing Direction	6 O'CLOCK							

Note 1: definition of viewing angle $\theta 1 \& \theta 2$ Note 2: definition of viewing angle $\theta 3 \& \theta 4$



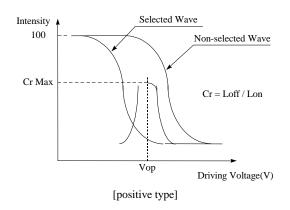


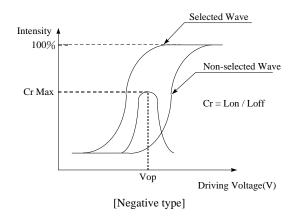
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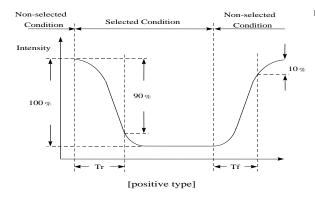


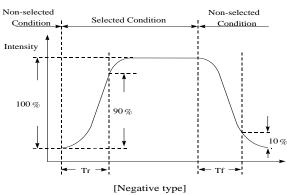
Note 3: definition of contrast ratio (CR)





Note 4: definition of response time





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6 BACKLIGHT SPECIFICATION

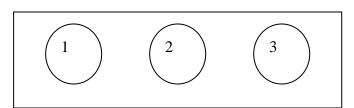
6.1 BACKLIGHT CHARACTERISTICS

Ta=25°C

Item	Symbol	Condition	Min	Тур	Max	Unit	Note
LED Input Voltage	V_{LED}	$I_{LED} = 120 \text{mA}$		5		V	
LED Input Current	I_{LED}			160		mA	
Average Brightness Ta=25°C IL=160mA	Test when connecting after 3min. (max contrast) White LED		450	550		cd/m²	3
Brightness Uniformity	$I_{LED} = 160 \text{mA}$		80			%	1, 3
Life time	$I_{LED} = 160 \text{mA}$			40000		hrs	2
Colour	White						

Note:

- 1. Brightness uniformity = $(MAX-MIN) / MAX \times 100$
- 2. Half of the original brightness
- 3. Average brightness of 3 points when the B/L is used at the beginning.



6.2 LABELLING & MARKING

DENSITRON LMR4755 Taiwan YYMM

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7 QUALITY ASSURANCE SPECIFICATION

7.1 CONFORMITY

The performance, function and reliability of the shipped products conform to the Product Specification.

7.2 DELIVERY ASSURANCE

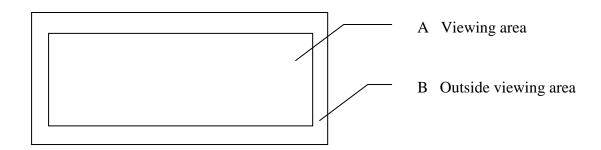
7.2.1 Delivery inspection standards

- MIL-STD-105E, general inspection level II, single sampling level;
- IPC-AA610 rev. C, class 2 electronic assemblies standard

The quality assurance levels are shown below:

Class	AQL (%)
Critical defect	0.65%
Major defect	1.0%
Minor defect	2.5%
TOTAL	2.5%

7.2.2 Zone definition



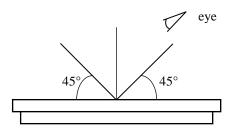
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7.2.3 Visual inspection

- Inspect under 2x20W or 40W fluorescent lamp (approximately 3000 lux) leaving 25 to 30 cm between the module and the lamp and 30 cm between the module and the eye (measuring position).
- Appearance is inspected at the best contrast voltage (best contrast is adjusted considering clearness and crosstalk on screen).
- Inspect the module at 45° right and left, top and bottom.
- Use the optimum viewing angle during the contrast inspection.



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7.2.3.1 Standard of appearance inspection

Units: mm

Class	Item			Criteria		
				Citteria		
Minor	Packing &		nside package		oduct no., lot no.,	<u> </u>
Critical	Label			ed with others and	quantity must not	be different from
			d on the labe			
Major	Dimension	Product dim	ensions must	be according to sp	pecification and di	rawing
Major	Electrical	Product elec	trical charact	eristics must be ac	cording to specifi	cation
Critical	LCD Display	Missing line	es or wrong pa	atterns on LCD dis	splay are not allow	ved
Minor	Black spot, white spot,	Round type: $\emptyset = (X+Y)/2$	as per follow 2	ving drawing		
	dust			A	cceptable quantity	/
				Size	Zone A	Zone B
			<u>+</u> _	Ø<0.1	Any number	
			Y	0.1<Ø<0.2	2	1
		1	 	0.2<Ø<0.25	1	Any number
		X		0.25<Ø	0	
		Line type: as	s per followir		ole quantity	
		∠ W	Length	Width	Zone A	Zone B
				W≤0.02	Any number	
			L≤3.0	0.02 <w≤0.03< td=""><td>2</td><td>Any number</td></w≤0.03<>	2	Any number
			L≤2.5	0.03 <w≤0.05< td=""><td></td><td>Thiy nameer</td></w≤0.05<>		Thiy nameer
		L		0.05 <w< td=""><td>As round type</td><td></td></w<>	As round type	
		Total acceptable quantity: 3				
Minor	Polariser			n is permitted		
Minon	scratch	_	olariser: sam	e as No. 1		
Minor	Polariser bubble	$\emptyset = (X+Y)/(X+Y)$	2	Α.	acantahla ayantite	
	Jubbie	Acceptable quantity Size Zone A Zone B				
			L	\varnothing <0.2	Any number	Zone D
			V	0.2<Ø<0.5	2	1
			T T	0.5<Ø<1.0	1	Any number
		X	•	1.0<Ø	0	1
		Total acceptable quantity: 3				
		Total acceptable qualitity. 3				
		I				

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Class	Item	Criteria			
Minor	Segment deformation	1.a. Pin hole on segmented	d display		
		W: segment width			
		$\emptyset = (A+B)/2$		Acceptable quantity	7
		B	Width	Ø	
			W≤0.4	Ø≤0.2 and	
			W>0.4	$\varnothing \leq 0.25$ and	
			Pin holes with	e quantity: 1 defect	
Minor	Segment	1b. Pin hole on dot matrix	display		
	deformation	₩ <0.	05	Acceptable	quantity
				Size a,b<0.1	A ny mumb on
) <u>d</u>	$(a+b)/2 \le 0.1$	Any number Any number
				$0.5 < \emptyset < 1.0$	3
				Total acceptable	quantity: 7
		3. Alignment layer defect $\emptyset = (a+b)/2$		Acceptable a \geq b a $<$ b Acceptable Size $\emptyset \leq 0.4$ $0.4 < \emptyset \leq 1.0$ $1.0 < \emptyset \leq 1.5$ $1.5 < \emptyset \leq 2.0$ Total acceptable	a/b≤4/3 a/b>4/3 e quantity Any number 5 3 2
Minor	Colour uniformity	Level of sample for approval set as limit sample			
Critical	Backlight	The backlight colour should correspond to the product specification			
Critical		Flashing and or unlit backlight is not allowed			
Minor		Dust larger than 0.25 mm is not allowed			
Major	COB	Exposed wire bond pad is not allowed			
Major		Insufficient covering with resin is not allowed (wire bond line exposed)			
Minor		Dust or bubble on the resin are not allowed			

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Class	Item	Criteria			
Major	PCB ~	No unmelted solde	r paste should be pre	esent on PCB	
Critical		Cold solder joints,	missing solder conn	ections, or oxidation	are not allowed
Minor		No residue or solder balls on PCB are allowed			
Critical	XX	Short circuits on components are not allowed			
Minor	Tray			Size	Quantity
	particles		On tray	Ø<0.2	Any number
			On tray	Ø>0.25	4
			On diamless	Ø≥0.25	2
			On display	L = 3	1

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7.3 DEALING WITH CUSTOMER COMPLAINTS

7.3.1 Non-conforming analysis

Purchaser should supply Densitron with detailed data of non-conforming sample. After accepting it, Densitron should complete the analysis in two weeks from receiving the sample.

If the analysis cannot be completed on time, Densitron must inform the purchaser.

7.3.2 Handling of non-conforming displays

If any non-conforming displays are found during customer acceptance inspection which Densitron is clearly responsible for, return them to Densitron.

Both Densitron and customer should analyse the reason and discuss the handling of non-conforming displays when the reason is not clear.

Equally, both sides should discuss and come to agreement for issues pertaining to modification of Densitron quality assurance standard.

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8 RELIABILITY SPECIFICATION

8.1 RELIABILITY TESTS

Test Item	Test Condition	Evaluation and assessment
High Temperature Operation	70°C ±2°C for 240 hours	No abnormalities in function* and appearance
Low Temperature Operation	-20°C ±2°C for 240 hours	No abnormalities in function* and appearance
Thermal Shock Storage	-30°C (30 min) ->25°C (5 min) - >80°C (30 min) ->25°C (5 min) 5 cycles	No abnormalities in function* and appearance
Vibration	10Hz ~ 55Hz 0.3mm / 1 Octave 55Hz ~ 500Hz 3g / 1 Octave 20 cycle / per axis	No abnormalities in function* and appearance

^{*} Current consumption 3 times initial value

8.2 LIFE TIME

Item	Description		
1	Function, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions of room temperature (25±10 °C), normal humidity (45±20% RH), and in area not exposed to direct sunlight.		
2	Function, performance, appearance, etc. shall be free from remarkable deterioration within 5,000 hours under ordinary operating and storage conditions of 70 °C temperature, normal humidity (45±20% RH), and in area not exposed to direct sunlight.		

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^{*} Contrast > ½ initial value



9 PART NUMBER DESCRIPTIONS FOR AVAILABLE OPTIONS

LMR4755①2240G3203④5

① POLARIZER TYPE

B = Transflective: light background with LED backlight

E = Transmissive: dark background with white LED backlight

② BACKLIGHT COLOUR

W = White

3 FLUID TYPE AND POWER SUPPLY

W = Wide Temperature Range; on-board negative supply voltage generator H = Wide Temperature Range

4 TEMPERATURE COMPENSATION

C = with on board temperature compensation circuitry

N = No on board temperature compensation and NTN Fluid

S FLUID TYPE

F = FSTN

B = STN (Blue Mode)

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10 HANDLING PRECAUTIONS

Safety

If the LCD panel breaks, be careful not to get the liquid crystal fluid in your mouth or in your eyes. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

Mounting and Design

Place a transparent plate (e.g. acrylic, polycarbonate or glass) on the display surface to protect the display from external pressure. Leave a small gap between the transparent plate and the display surface.

When assembling with a zebra connector, clean the surface of the pads with alcohol and keep the surrounding air very clean.

Design the system so that no input signal is given unless the power supply voltage is applied.

Caution during LCD cleaning

Lightly wipe the display surface with a soft cloth soaked with Isopropyl alcohol, Ethyl alcohol or Trichlorotriflorothane.

Do not wipe the display surface with dry or hard materials that will damage the polariser surface. Do not use aromatic solvents (toluene and xylene), or ketonic solvents (ketone and acetone).

Caution against static charge

As the display uses C-MOS LSI drivers, connect any unused input terminal to VDD or VSS. Do not input any signals before power is turned on.

Also, ground your body, work/assembly table and assembly equipment to protect against static electricity.

Packaging

Displays use LCD elements, and must be treated as such. Avoid strong shock and drop from a height. To prevent displays from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity.

Caution during operation

It is indispensable to drive the display within the specified voltage limit since excessive voltage shortens its life.

Direct current causes an electrochemical reaction with remarkable deterioration of the display quality. Give careful consideration to prevent direct current during ON/OFF timing and during operation.

Response time is extremely delayed at temperatures lower than the operating temperature range while, at high temperatures, displays become dark. However, this phenomenon is reversible and does not mean a malfunction or a display that has been permanently damaged.

If the display area is pushed on hard during operation, some graphics will be abnormally displayed but returns to a normal condition after turning off the display once.

Even a small amount of condensation on the contact pads (terminals) can cause an electro-chemical reaction which causes missing rows and columns. Give careful attention to avoid condensation.

Storage

Store the display in a dark place where the temperature is $25^{\circ}C \pm 10^{\circ}C$ and the humidity below 50% RH

Store the display in a clean environment, free from dust, organic solvents and corrosive gases. Do not crash, shake or jolt the display (including accessories).

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