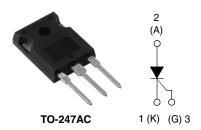


Vishay High Power Products

Phase Control SCR, 20 A



PRODUCT SUMMARY				
V _T at 20 A	< 1.3 V			
I _{TSM}	300 A			
V_{RRM}	800/1200 V			

DESCRIPTION/FEATURES



The 30TPS...PbF High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

RoHS*

Typical applications are in input rectification (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level and lead (Pb)-free ("PbF" suffix).

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I _{T(AV)}	Sinusoidal waveform	20	۸		
I _{RMS}		30	Α		
V _{RRM} /V _{DRM}		800/1200	V		
I _{TSM}		300	А		
V _T	20 A, T _J = 25 °C	1.3	V		
dV/dt		500	V/μs		
dl/dt		150	A/μs		
T _J		- 40 to 125	°C		

VOLTAGE RATINGS			
PART NUMBER	V _{RRM} /V _{DRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA
30TPS08PbF	800	10	
30TPS12PbF	1200	1300	10

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS		
Maximum average on-state current	I _{T(AV)}	T _C = 95 °C, 180° conduc	ction half sine wave	20			
Maximum RMS on-state current	I _{RMS}			30	Α		
Maximum peak, one-cycle	I	10 ms sine pulse, rated	V _{RRM} applied	250	A		
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no vol	tage reapplied	300			
Maximum 12+ for fusing	l²t	10 ms sine pulse, rated	V _{RRM} applied	310	A ² s		
Maximum I ² t for fusing	I-t	10 ms sine pulse, no vol	10 ms sine pulse, no voltage reapplied				
Maximum I $^2\sqrt{t}$ for fusing	I ² √t	t = 0.1 to 10 ms, no voltage reapplied		4420	A²√s		
Maximum on-state voltage drop	V_{TM}	20 A, T _J = 25 °C		1.3	V		
On-state slope resistance	r _t	T 105.00		12	mΩ		
Threshold voltage	V _{T(TO)}	- T _J = 125 °C		1.0	V		
Maximum various and divast lackage current	1 //	T _J = 25 °C	V Dated V A/	0.5	0.5		
Maximum reverse and direct leakage current	I_{RM}/I_{DM}	$V_R = Rated V_{RRM}/V_{DRM}$		10	A		
Maximum holding current	l _Η	Anode supply = 6 V, resistive load, initial $I_T = 1$ A		100	mA		
Maximum latching current	ΙL	Anode supply = 6 V, resistive load		200			
Maximum rate of rise of off-state voltage	dV/dt			500	V/µs		
Maximum rate of rise of turned-on current	dl/dt			150	A/μs		

TRIGGERING						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum peak gate power	P_{GM}		8.0	W		
Maximum average gate power	$P_{G(AV)}$		2.0	VV		
Maximum peak positive gate current	+ I _{GM}		1.5	Α		
Maximum peak negative gate voltage	- V _{GM}		10	V		
	I _{GT}	Anode supply = 6 V, resistive load, T _J = - 10 °C	60			
Maximum required DC gate current to trigger		Anode supply = 6 V, resistive load, T _J = 25 °C	45	mA		
		Anode supply = 6 V, resistive load, T _J = 125 °C	20			
		Anode supply = 6 V, resistive load, T _J = - 10 °C	2.5			
Maximum required DC gate voltage to trigger	V_{GT}	Anode supply = 6 V, resistive load, T _J = 25 °C	y = 6 V, resistive load, T _J = 25 °C 2.0			
voltage to trigger		Anode supply = 6 V, resistive load, T _J = 125 °C	1.0	V		
Maximum DC gate voltage not to trigger	V_{GD}	T. – 105 °C V Potod volue	0.25			
Maximum DC gate current not to trigger	I_{GD}	T _J = 125 °C, V _{DRM} = Rated value	2.0	mA		

SWITCHING						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Typical turn-on time	t _{gt}	T _J = 25 °C	0.9			
Typical reverse recovery time	t _{rr}	T 105 °C	4	μs		
Typical turn-off time	ta	T _J = 125 °C	110			



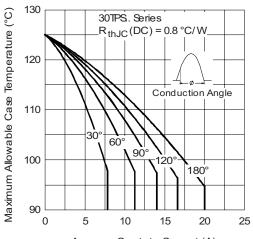
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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 125	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	0.8	°C/W
Maximum thermal resistance, junction to ambient		R _{thJA}	Do operation	40	
Maximum thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.2	
Approximate weight				6	g
Approximate weight				0.21	OZ.
minimum				6 (5)	kgf · cm
Mounting torque —	maximum			12 (10)	(lbf \cdot in)
Marking device			O TO 04740 (JEDEO)	30TPS08	
			Case style TO-247AC (JEDEC)	30TF	30TPS12

Document Number: 94386 Revision: 06-Jun-08

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Average On-state Current (A) Fig. 1 - Current Rating Characteristics

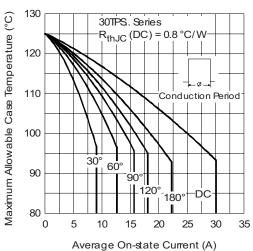


Fig. 2 - Current Rating Characteristics

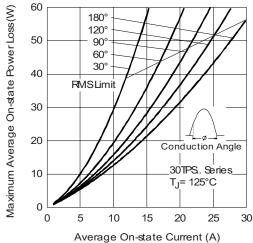


Fig. 3 - On-State Power Loss Characteristics

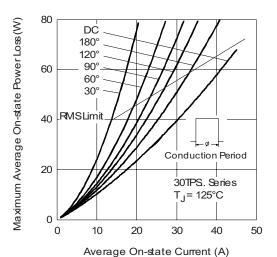


Fig. 4 - On-State Power Loss Characteristics

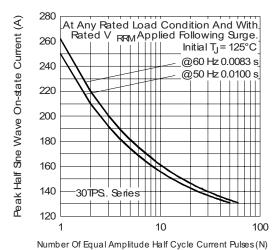


Fig. 5 - Maximum Non-Repetitive Surge Current

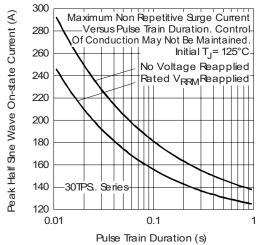


Fig. 6 - Maximum Non-Repetitive Surge Current



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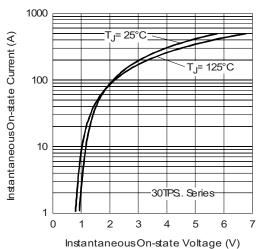


Fig. 7 - On-State Voltage Drop Characteristics

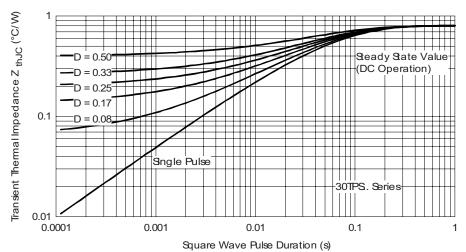


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

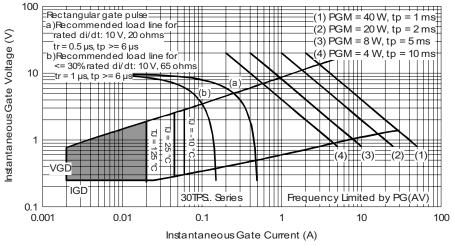


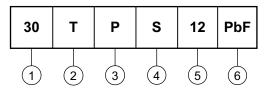
Fig. 9 - Gate Characteristics

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ORDERING INFORMATION TABLE

Device code



- 1 Current rating (30 = 30 A)
- **2** Circuit configuration:

T = Thyristor

- 3 Package:
 - P = TO-247
- 4 Type of silicon:

S = Standard recovery rectifier

None = Standard production

• PbF = Lead (Pb)-free

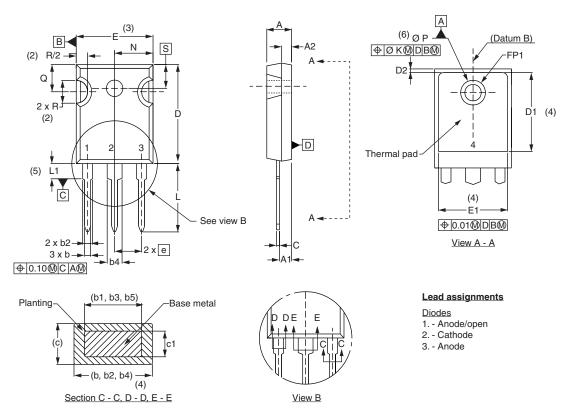
LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95223			
Part marking information	http://www.vishay.com/doc?95226		

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Vishay Semiconductors

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215	BSC	
FK	2.	54	0.0	010	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62	BSC	0	.3	
ΦР	3.56	3.66	0.14	0.144	
ФР1	1	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51	BSC	0.217	'BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c





Vishay

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Revision: 11-Mar-11