

C3D10065ASilicon Carbide Schottky Diode

Z-RECTM RECTIFIER

V_{RRM} = 650 V $I_{F}(T_{c}=135^{\circ}C) = 14 \text{ A}$ Q_{c} = 25 nC

Features

- 650-Volt Schottky Rectifier
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on V_E

Package







TO-220-2

Benefits

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway



Applications

- Switch Mode Power Supplies
- Power Factor Correction
 - Typical PFC P_{out}: 1000W-2000W
- Motor Drives
 - Typical Power : 3HP-5HP

Part Number	Package	Marking	
C3D10065A	TO-220-2	C3D10065	

Maximum Ratings ($T_c = 25$ °C unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V_{RRM}	Repetitive Peak Reverse Voltage	650	V		
V_{RSM}	Surge Peak Reverse Voltage	650	V		
V _{DC}	DC Blocking Voltage	650	V		
$\mathrm{I}_{\scriptscriptstyle{F}}$	Continuous Forward Current	29.5 14 10	А	T _c =25°C T _c =135°C T _c =152°C	
\mathbf{I}_{FRM}	Repetitive Peak Forward Surge Current	67 44	А	T_c =25°C, t_p =10 ms, Half Sine Wave, D=0.3 T_c =110°C, t_p =10 ms, Half Sine Wave, D=0.3	
\mathbf{I}_{FSM}	Non-Repetitive Peak Forward Surge Current	90 71	А	T_c =25°C, t_p =10ms, Half Sine Wave, D=0.3 T_c =110°C, t_p =10 ms, Half Sine Wave, D=0.3	
\mathbf{I}_{FSM}	Non-Repetitive Peak Forward Surge Current	250	Α	$T_c = 25$ °C, $t_p = 10$ µs, Pulse	
P_{tot}	Power Dissipation	136.3 59	W	T _c =25°C T _c =110°C	
$T_{_{\mathtt{J}}}$, $T_{_{\mathtt{stg}}}$	Operating Junction and Storage Temperature	-55 to +175	°C		
-	TO-220 Mounting Torque	1 8.8	Nm lbf-in	M3 Screw 6-32 Screw	



Electrical Characteristics

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V _F	Forward Voltage	1.5 2.0	1.8 2.4	V	$I_F = 10 \text{ A } T_J = 25^{\circ}\text{C}$ $I_F = 10 \text{ A } T_J = 175^{\circ}\text{C}$	
I _R	Reverse Current	12 24	60 220	μΑ	$V_R = 650 \text{ V } T_J = 25^{\circ}\text{C}$ $V_R = 650 \text{ V } T_J = 175^{\circ}\text{C}$	
Q _c	Total Capacitive Charge	25		nC	$V_R = 650 \text{ V}, I_F = 10 \text{ A}$ $di/dt = 500 \text{ A}/\mu\text{s}$ $T_J = 25^{\circ}\text{C}$	
С	Total Capacitance	480 50 42		pF	$V_R = 0 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$ $V_R = 200 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$ $V_R = 400 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$	

Note:

Thermal Characteristics

Symbol	Parameter	Тур.	Unit
$R_{_{ heta JC}}$	Thermal Resistance from Junction to Case	1.1	°C/W

Typical Performance

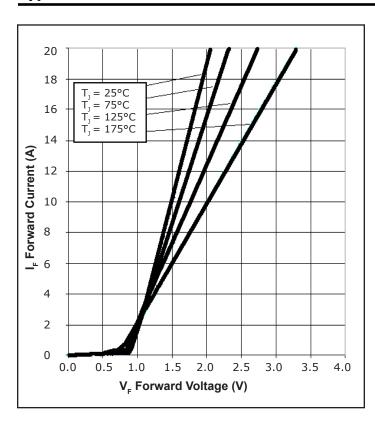


Figure 1. Forward Characteristics

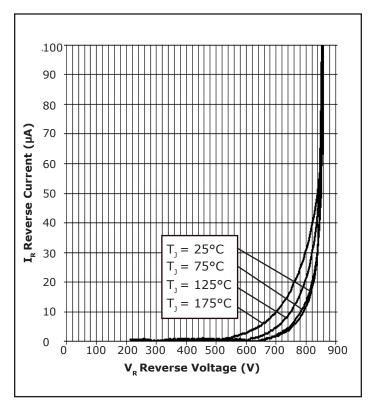
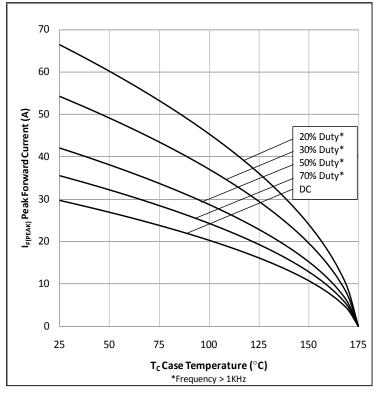


Figure 2. Reverse Characteristics

^{1.} This is a majority carrier diode, so there is no reverse recovery charge.



Typical Performance



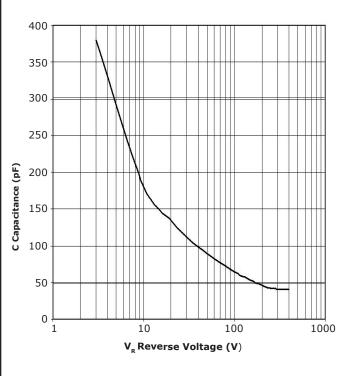


Figure 3. Current Derating

Figure 4. Capacitance vs. Reverse Voltage

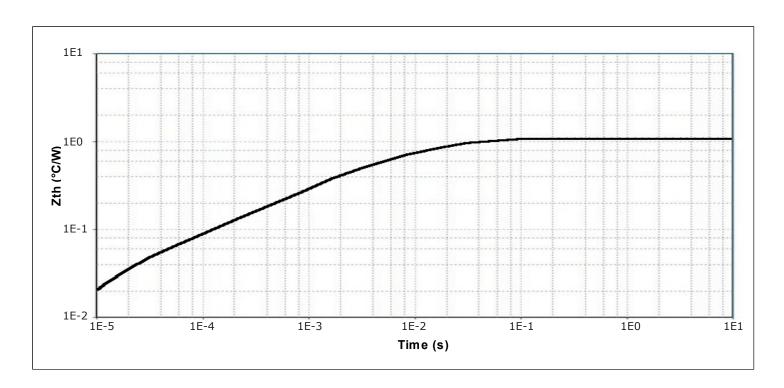


Figure 5. Transient Thermal Impedance



Typical Performance

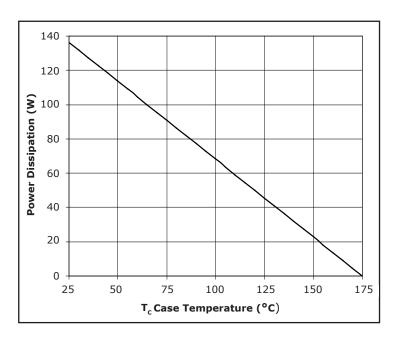


Figure 6. Power Derating

Diode Model

$$\begin{array}{c|c} - & & \\ \hline V_T & R_T \end{array}$$

$$Vf_T = V_T + If^*R_T$$

$$V_T = 0.98 + (T_J^* - 1.6^*10^{-3})$$

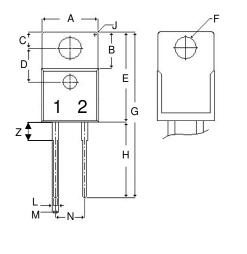
$$R_T = 0.04 + (T_J^* 0.522^*10^{-3})$$

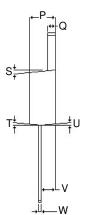
Note: T_j = Diode Junction Temperature In Degrees Celsius



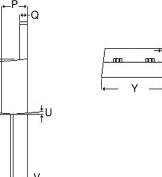
Package Dimensions

Package TO-220-2





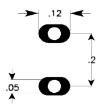




	POS	Inc	hes	Millimeters		
	POS	Min	Max	Min	Max	
	А	.381	.410	9.677	10.414	
	В	.235	.255	5.969	6.477	
	С	.100	.120	2.540	3.048	
	D	.223	.337	5.664	8.560	
	Е	.590	.615	14.986	15.621	
	F	.143	.153	3.632	3.886	
	G	1.105	1.147	28.067	29.134	
	Н	.500	.550	12.700	13.970	
	J	R 0.	.197	R 0.	197	
	L	.025	.036	.635	.914	
	М	.045	.055	1.143	1.397	
	N	.195	.205	4.953	5.207	
	Р	.165	.185	4.191	4.699	
	Q	.048	.054	1.219	1.372	
	S	3°	6°	3°	6°	
	T	3°	6°	3°	6°	
	U	3°	6°	3°	6°	
	V	.094	.110	2.388	2.794	
	W	.014	.025	.356	.635	
	Х	3°	5.5°	3°	5.5°	
	Υ	.385	.410	9.779	10.414	
	Z	.130	.150	3.302	3.810	
i	NOTE:					

1. Dimension L, M, W apply for Solder Dip Finish

Recommended Solder Pad Layout



TO-220-2

Part Number	Package	Marking	
C3D10065A	TO-220-2	C3D10065	

Note: Recommended soldering profiles can be found in the applications note here: http://www.cree.com/power_app_notes/soldering





Notes

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Cree representative or from the Product Documentation sections of www.cree.com.

REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Cree representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

• This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, air traffic control systems, or weapons systems.