

V <sub>DSS</sub>	650V
R <sub>DS(on)</sub> (Typ.)	120mΩ
I <sub>D</sub>	29A
P <sub>D</sub>	165W

#### Features

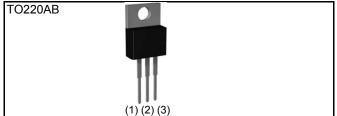
- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating ; RoHS compliant

#### Application

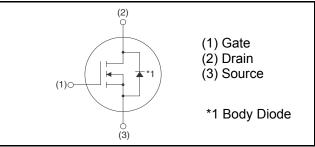
- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives

## •Absolute maximum ratings (T<sub>a</sub> = 25°C)

## •Outline



#### Inner circuit



#### Packaging specifications

	Packing	Tube
	Reel size (mm)	-
Tuno	Tape width (mm)	-
Туре	Basic ordering unit (pcs)	50
	Packing code	С
	Marking	SCT2120AF

Parameter		Symbol	Value	Unit
Drain - Source voltage		V <sub>DSS</sub>	650	V
Continuous drain current	$T_c = 25^{\circ}C$	ا <sub>D</sub> *1	29	A
	T <sub>c</sub> = 100°C	ا <sub>D</sub> *1	20	А
Pulsed drain current		I <sub>D,pulse</sub> *2	72	А
Gate - Source voltage (DC)		V <sub>GSS</sub>	-6 to 22	V
Gate - Source surge voltage (T <sub>surge</sub> < 300nsec)		V <sub>GSS-surge</sub> *3	-10 to 26	V
Power dissipation ( $T_c = 25^{\circ}C$ )		P <sub>D</sub>	165	W
Junction temperature		Tj	175	°C
Range of storage temperature		T <sub>stg</sub>	-55 to +175	°C

## •Thermal resistance

Parameter	Symbol	Values			Unit
Faranielei	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub>	-	0.70	0.91	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

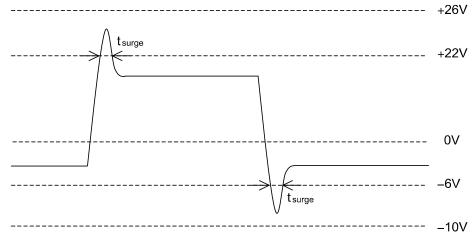
## •Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol	Symbol Conditions		Values		
Faranieter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	$V_{(BR)DSS}$	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	650	-	-	V
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{DS} = 650V, V_{GS} = 0V$ $T_j = 25^{\circ}C$ $T_j = 150^{\circ}C$	-	1 2	10 -	μA
Gate - Source leakage current	I <sub>GSS+</sub>	$V_{GS}$ = +22V, $V_{DS}$ = 0V	-	-	100	nA
Gate - Source leakage current	I <sub>GSS-</sub>	$V_{GS}$ = -6V, $V_{DS}$ = 0V	-	-	-100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 3.3 \text{mA}$	1.6	2.8	4.0	V

\*1 Limited only by maximum temperature allowed.

\*2 PW  $\leq$  10 $\mu s,$  Duty cycle  $\leq$  1%

\*3 Example of acceptable Vgs waveform



\*4 Pulsed

## •Electrical characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	Symbol	Conditions		Values		Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
		V <sub>GS</sub> = 18V, I <sub>D</sub> = 10A				
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	T <sub>j</sub> = 25°C	-	120	156	mΩ
		T <sub>j</sub> = 125°C	-	149	-	
Gate input resistance	R <sub>G</sub>	f = 1MHz, open drain	-	13.8	-	Ω
Transconductance	g <sub>fs</sub> *4	V <sub>DS</sub> = 10V, I <sub>D</sub> = 10A	-	2.7	-	S
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	1200	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 500V	-	90	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	13	-	
Effective output capacitance, energy related	C <sub>o(er)</sub>	V <sub>GS</sub> = 0V V <sub>DS</sub> = 0V to 300V	-	115	-	pF
Turn - on delay time	t <sub>d(on)</sub> *4	V <sub>DD</sub> = 300V, I <sub>D</sub> = 10A	-	22	-	
Rise time	t <sub>r</sub> *4	V <sub>GS</sub> = 18V/0V	-	31	-	
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L = 30\Omega$	-	60	-	ns
Fall time	t <sub>f</sub> *4	$R_{G} = 0\Omega$	-	19	-	
Turn - on switching loss	E <sub>on</sub> *4	$V_{DD} = 300V, I_{D} = 10A$ $V_{GS} = 18V/0V$	-	61	-	1
Turn - off switching loss	E <sub>off</sub> *4	R <sub>G</sub> = 0Ω, L=500µH *E <sub>on</sub> includes diode reverse recovery	-	41	-	μJ

# •Gate Charge characteristics ( $T_a = 25^{\circ}C$ )

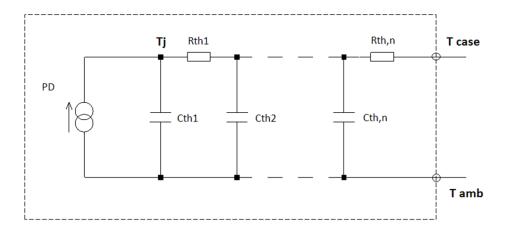
Parameter	Symbol	Conditions		Unit		
Faranielei	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	$Q_g^{*4}$	V <sub>DD</sub> = 300V	-	61	-	
Gate - Source charge	Q <sub>gs</sub> <sup>*4</sup>	I <sub>D</sub> = 10A	-	14	-	nC
Gate - Drain charge	$Q_{gd}^{*4}$	V <sub>GS</sub> = 18V	-	21	-	
Gate plateau voltage	V <sub>(plateau)</sub>	$V_{DD} = 300V, I_D = 10A$	-	10.4	-	V

•Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions		Unit		
Faranielei	Symbol	Conditions	Min.	Тур.	Max.	Unit
Inverse diode continuous, forward current	ا <sub>S</sub> *1	T <sub>c</sub> = 25°C	-	-	29	А
Inverse diode direct current, pulsed	I <sub>SM</sub> *2	T <sub>c</sub> = 25 C	-	-	72	А
Forward voltage	$V_{SD}$ *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = 10A	-	4.3	-	V
Reverse recovery time	t <sub>rr</sub> *4		-	33	-	ns
Reverse recovery charge	Q <sub>rr</sub> <sup>*4</sup>	I <sub>F</sub> = 10A, V <sub>R</sub> = 400V di/dt = 160A/μs	-	53	-	nC
Peak reverse recovery current	<sup>*4</sup>		-	3.0	-	А

## •Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R <sub>th1</sub>	96.1m		C <sub>th1</sub>	1.55m	
R <sub>th2</sub>	404m	K/W	C <sub>th2</sub>	5.23m	Ws/K
R <sub>th3</sub>	196m		C <sub>th3</sub>	83.3m	



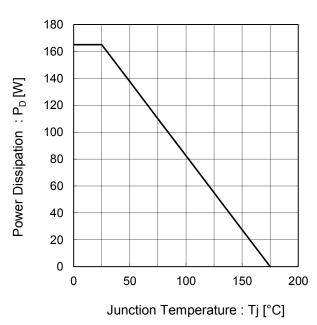


Fig.1 Power Dissipation Derating Curve

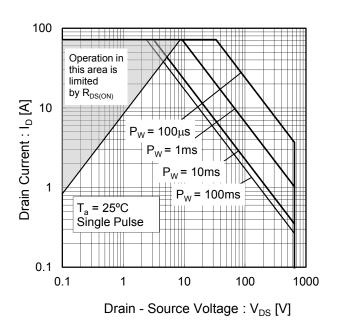
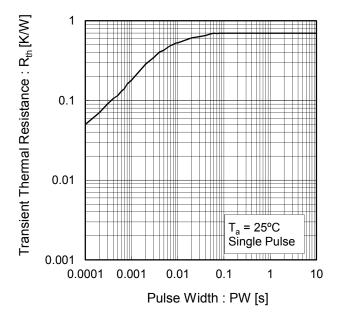


Fig.2 Maximum Safe Operating Area

Fig.3 Typical Transient Thermal Resistance vs. Pulse Width



28

26

24

22

20

₹\_<sub>18</sub>

Drain Current : I<sub>D</sub>

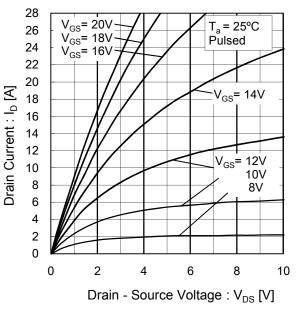
4

2

0

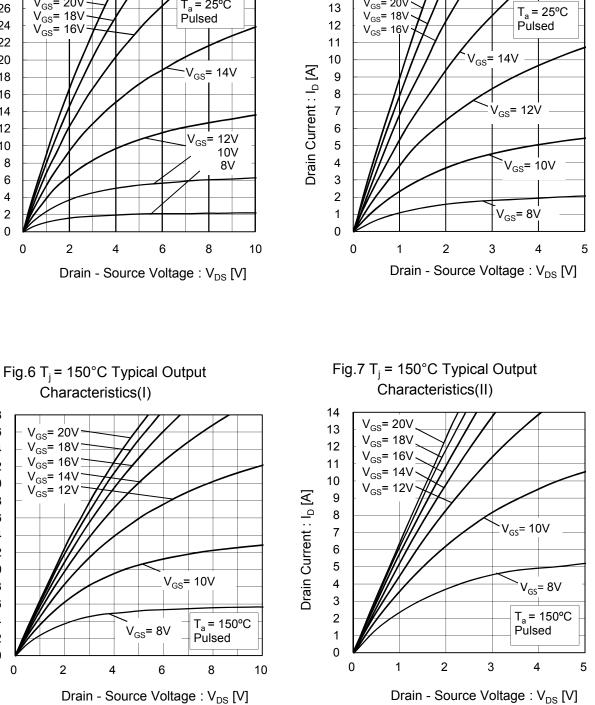
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#### Electrical characteristic curves



#### Fig.4 Typical Output Characteristics(I)

Fig.5 Typical Output Characteristics(II)



14

 $V_{GS}$ = 20V

2

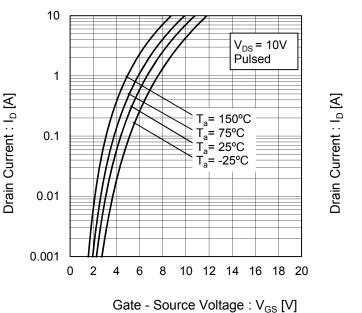


Fig.8 Typical Transfer Characteristics (I)

Fig.9 Typical Transfer Characteristics (II)

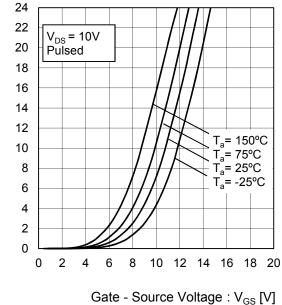
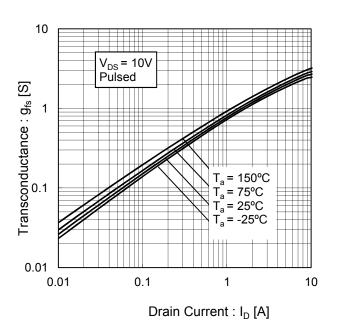
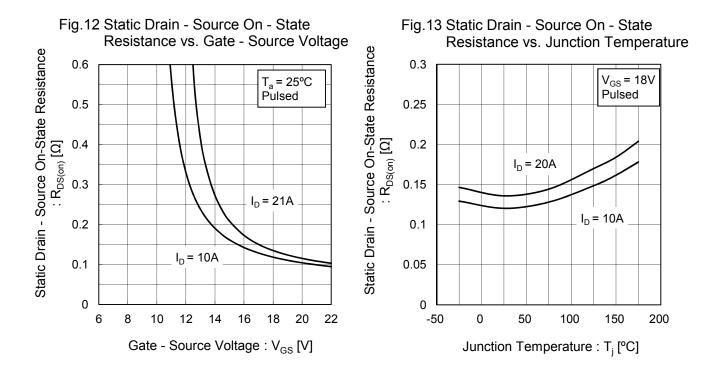
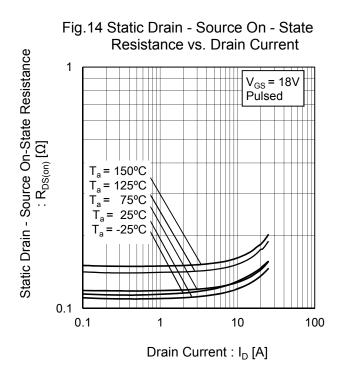


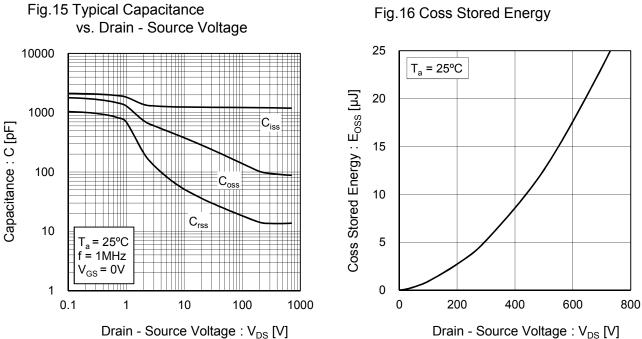
Fig.10 Gate Threshold Voltage vs. Junction Temperature 5  $V_{DS} = V_{GS}$  $I_D = 3.3 \text{mA}$ 4.5 Gate Threshold Voltage : V <sub>GS(th)</sub> [V] 4 3.5 3 2.5 2 1.5 1 0.5 0 -50 0 50 100 150 200 Junction Temperature : T<sub>i</sub> [°C]

Fig.11 Transconductance vs. Drain Current



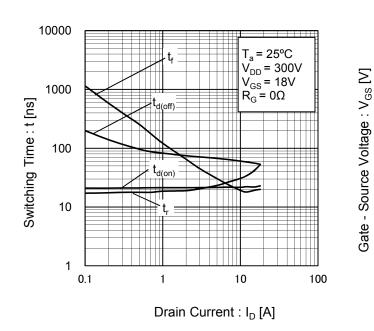


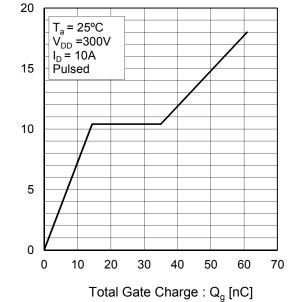


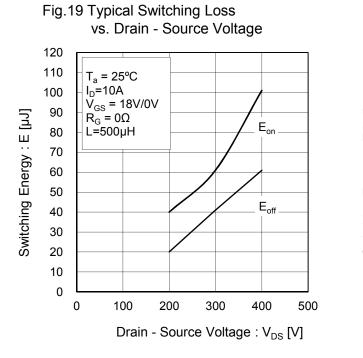


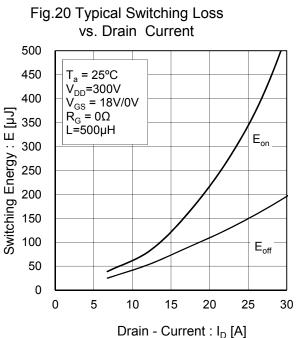
## Fig.17 Switching Characteristics

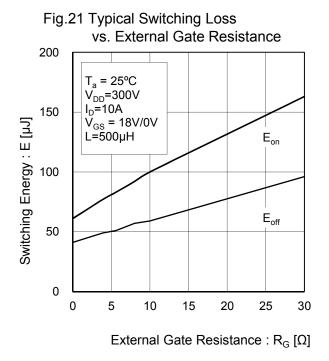
## Fig.18 Dynamic Input Characteristics

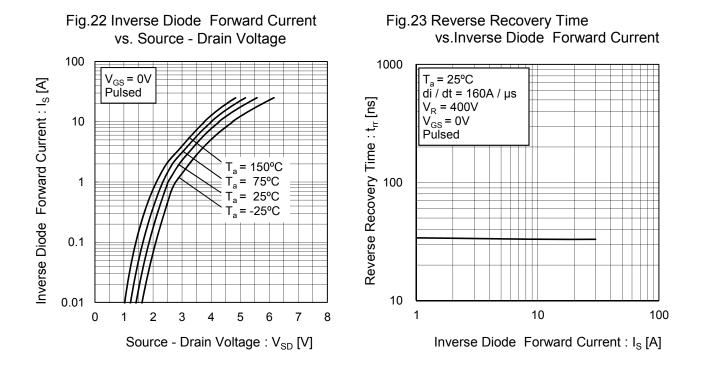














#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

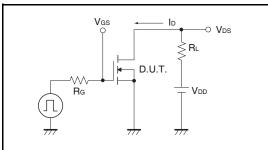


Fig.2-1 Gate Charge Measurement Circuit

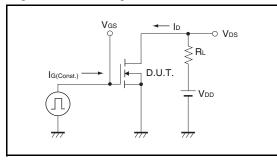


Fig.3-1 Switching Energy Measurement Circuit

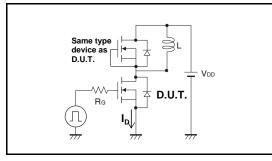
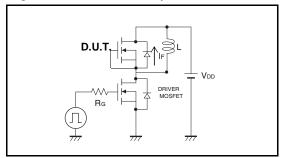


Fig.4-1 Reverse Recovery Time Measurement Circuit Fig.4-2 Reverse Recovery Waveform



#### Fig.1-2 Switching Waveforms

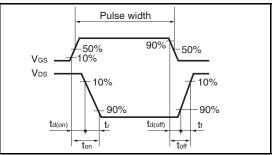


Fig.2-2 Gate Charge Waveform

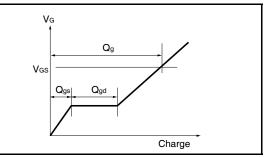
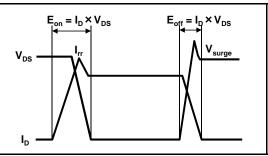
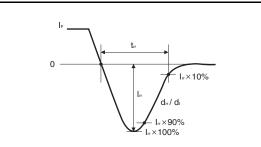


Fig.3-2 Switching Waveforms







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