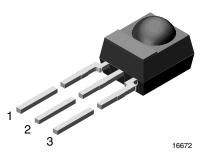


www.vishay.com

Vishay Semiconductors

IR Receiver Modules for Remote Control Systems



MECHANICAL DATA

Pinning for TSOP41.., TSOP43.., TSOP45..: 1 = OUT, 2 = GND, $3 = V_S$ Pinning for TSOP21.., TSOP23.., TSOP25..: 1 = OUT, $2 = V_S$, 3 = GND

Please see the document "Product Transition Schedule" at <u>www.vishay.com/ir-receiver-modules/</u> for up-to-date info, when this product will be released.

FEATURES

- · Low supply current
- Photo detector and preamplifier in one package
- Internal filter for PCM frequency
- · Improved shielding against EMI
- Supply voltage: 2.5 V to 5.5 V

APPLICATION CIRCUIT

IR receiver

Circuit

The external components R1 and C1 are optional

(typical values are $R_1 = 100 \Omega$, $C_1 = 0.1 \mu$ F).

to improve the robustness against electrical overstress

17170-10

Transmitter

with

TSALxxxx

- · Improved immunity against ambient light
- Insensitive to supply voltage ripple and noise
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

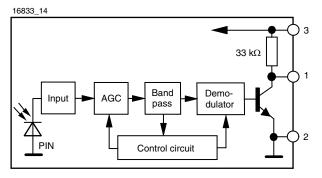
These products are miniaturized receivers for infrared remote control systems. A PIN diode and a preamplifier are assembled on a lead frame, the epoxy package acts as an IR filter.

The demodulated output signal can directly be decoded by a microprocessor. The main benefit of the TSOP41.., TSOP21.. is the compatibility to all IR remote control data formats. The TSOP43.., TSOP23.. are optimized to better suppress spurious pulses from fluorescent lamps and LCD TVs. The TSOP45.., TSOP25.. have an excellent noise suppression, it is immune against any dimmed LCD backlighting or plasma TVs. AGC3 and AGC5 may also suppress some data signals in case of continuous transmission.

This component has not been qualified according to automotive specifications.

PARTS TABLE							
CARRIER FREQUENCY	SHORT BURST AND HIGH DATA RATE (AGC1)		NOISY ENVIROMENTS AND SHORT BURSTS (AGC3)		VERY NOISY ENVIRONMENTS AND SHORT BURSTS (AGC5)		
	PINNING						
	$1 = OUT, 2 = GND, 3 = V_S$	1 = OUT, 2 = V _S , 3 = GND	$1 = OUT, 2 = GND, 3 = V_S$	$\begin{array}{c} 1 = \text{OUT}, \ 2 = \text{V}_{\text{S}}, \\ 3 = \text{GND} \end{array}$	$1 = OUT, 2 = GND, 3 = V_S$	$\begin{array}{l} 1 = \text{OUT}, 2 = \text{V}_{\text{S}}, \\ 3 = \text{GND} \end{array}$	
30 kHz	TSOP4130	TSOP2130	TSOP4330	TSOP2330	TSOP4530	TSOP2530	
33 kHz	TSOP4133	TSOP2133	TSOP4333	TSOP2333	TSOP4533	TSOP2533	
36 kHz	TSOP4136	TSOP2136	TSOP4336	TSOP2336	TSOP4536	TSOP2536	
38 kHz	TSOP4138	TSOP2138	TSOP4338	TSOP2338	TSOP4538	TSOP2538	
40 kHz	TSOP4140	TSOP2140	TSOP4340	TSOP2340	TSOP4540	TSOP2540	
56 kHz	TSOP4156	TSOP2156	TSOP4356	TSOP2356	TSOP4556	TSOP2556	

BLOCK DIAGRAM



Rev. 1.1, 04-Sep-12

1 For technical questions, contact: IRR@vishay.com Document Number: 82460

Vo

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GND

e Pb-free



RoHS COMPLIANT

TSOP21.., TSOP23.., TSOP41.., TSOP43.., TSOP25.., TSOP45..

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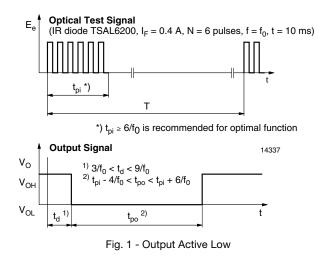
ABSOLUTE MAXIMUM RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Supply voltage		Vs	- 0.3 to + 6	V		
Supply current		ا _S	5	mA		
Output voltage		Vo	- 0.3 to 5.5	V		
Voltage at output to supply		V _S - V _O	- 0.3 to (V _S + 0.3)	V		
Output current		Ι _Ο	5	mA		
Junction temperature		Tj	100	°C		
Storage temperature range		T _{stg}	- 25 to + 85	°C		
Operating temperature range		T _{amb}	- 25 to + 85	°C		
Power consumption	T _{amb} ≤ 85 °C	P _{tot}	10	mW		
Soldering temperature	$t \le 10$ s, 1 mm from case	T _{sd}	260	°C		

Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

ELECRTICAL AND OPTICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply ourrept	$E_v = 0, V_S = 5 V$	I _{SD}	0.55	0.7	0.9	mA
Supply current	$E_v = 40$ klx, sunlight	I _{SH}		0.8		mA
Supply voltage		VS	2.5		5.5	V
Transmission distance	$E_v = 0$, test signal see fig. 1, IR diode TSAL6200, $I_F = 200 \text{ mA}$	d		45		m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see fig. 1	V _{OSL}			100	mV
Minimum irradiance	Pulse width tolerance: t _{pi} - 5/f _o < t _{po} < t _{pi} + 6/f _o , test signal see fig. 1	E _{e min.}		0.12	0.25	mW/m ²
Maximum irradiance	$\begin{array}{c} t_{pi} \text{ - } 5/f_o < t_{po} < t_{pi} + 6/f_o, \\ \text{test signal see fig. 1} \end{array}$	E _{e max.}	50			W/m ²
Directivity	Angle of half transmission distance	φ1/2		± 45		deg

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)



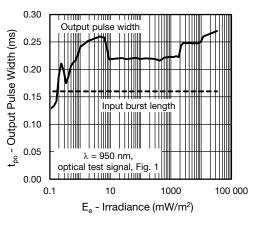


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient

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TSOP21.,, TSOP23.,, TSOP41.,, TSOP43.,, TSOP25.,, TSOP45..

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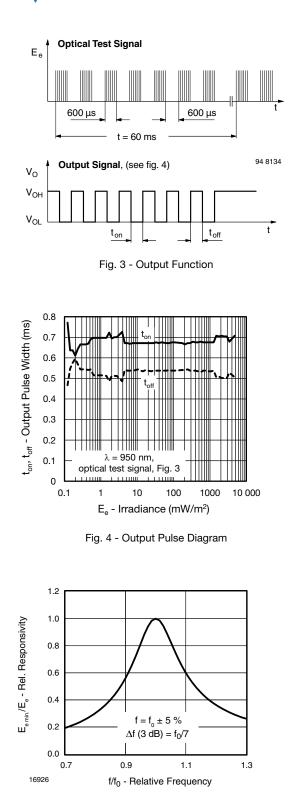
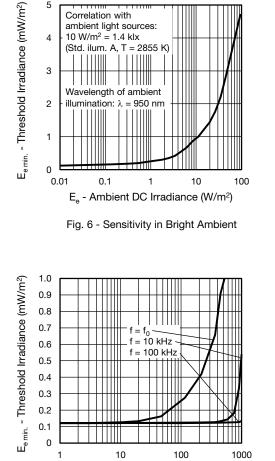
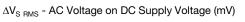


Fig. 5 - Frequency Dependence of Responsivity







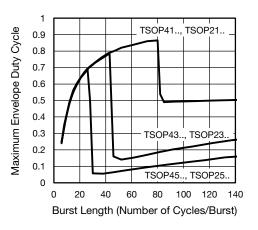


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length



TSOP21.., TSOP23.., TSOP41.., TSOP43.., TSOP25.., TSOP45..

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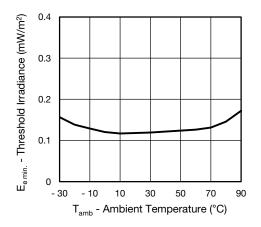


Fig. 9 - Sensitivity vs. Ambient Temperature

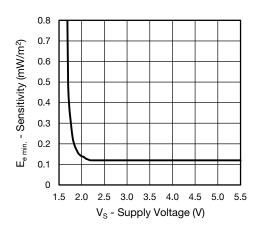


Fig. 12 - Sensitivity vs. Supply Voltage

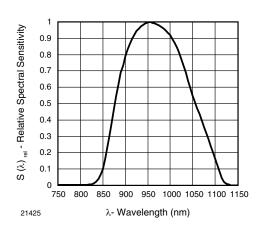


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

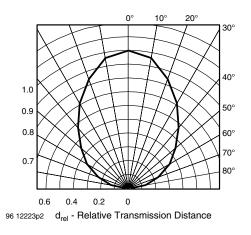


Fig. 11 - Horizontal Directivity

TSOP21.,, TSOP23.,, TSOP41.,, TSOP43.,, TSOP25.,, TSOP45..



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SUITABLE DATA FORMAT

These products are designed to suppress spurious output pulses due to noise or disturbance signals. Data and disturbance signals can be distinguished by the devices according to carrier frequency, burst length and envelope duty cycle. The data signal should be close to the band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the IR receiver in the presence of a disturbance signal, the sensitivity of the receiver is reduced to insure that no spurious pulses are present at the output. Some examples of disturbance signals which are suppressed are:

- DC light (e.g. from tungsten bulb or sunlight)
- Continuous signals at any frequency
- Modulated noise from fluorescent lamps with electronic ballasts (see figure 14 or figure 15)

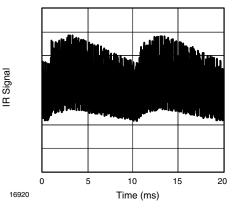


Fig. 13 - IR Signal from Fluorescent Lamp with Low Modulation

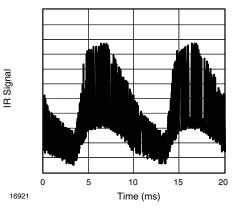


Fig. 14 - IR Signal from Fluorescent Lamp with High Modulation

	TSOP41, TSOP21	TSOP43, TSOP23	TSOP45, TSOP25
Minimum burst length	6 cycles/burst	6 cycles/burst	6 cycles/burst
After each burst of length a minimum gap time is required of	6 to 70 cycles ≥ 10 cycles	6 to 35 cycles ≥ 10 cycles	6 to 24 cycles ≥ 10 cycles
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 1.1 x burst length	35 cycles > 6 x burst length	24 cycles > 25 ms
Maximum number of continuous short bursts/second	2000	2000	2000
Recommended for NEC code	yes	yes	yes
Recommended for RC5/RC6 code	yes	yes	yes
Recommended for Sony code	yes	no	no
Recommended for RECS-80 code	yes	yes	yes
Recommended for RCMM code	yes	yes	yes
Recommended for r-step code	yes	yes	yes
Recommended for XMP code	yes	yes	yes
Suppression of interference from fluorescent lamps	Common disturbance signals are supressed (example: signal pattern of fig. 14)	Internference signals from lamps with high modulation are suppressed (examples: signal pattern of fig. 14 and fig. 15)	Even critical disturbanc signals like dimmed LCD backlighting are suppressed

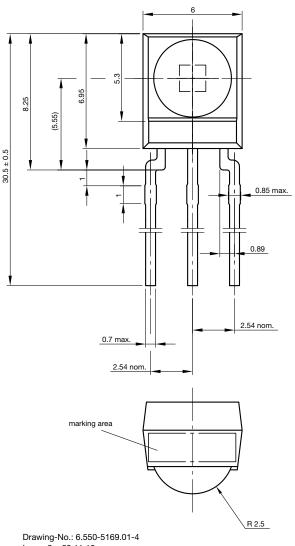
Note

• For data formats with long bursts (more than 10 carrier cycles) please see the datasheet for TSOP22.., TSOP48.., TSOP44...

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PACKAGE DIMENSIONS in millimeters



0.5 max.

Not indicated tolerances ± 0.2



Drawing-No.: 6.550-5169.01-4 Issue: 9; 03.11.10 13655



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