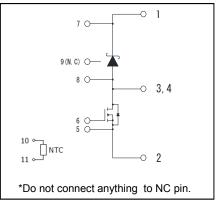
Application

- · Motor drive
- · Converter
- · Photovoltaics, wind power generation.

Features

- 1) Low surge, low switching loss.
- 2) High-speed switching possible.
- 3) Reduced temperature dependence.

●Circuit diagram



Construction

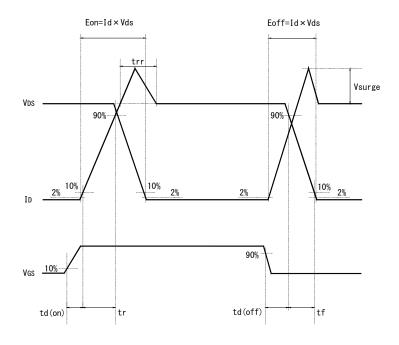
This product is a chopper module consisting of SiC-UMOSFET and SiC-SBD from ROHM.

● Absolute maximum ratings (T_j = 25°C)

Parameter	Symbol	Conditions	Limit	Unit	
Drain-source voltage	V_{DSS}	G-S short	1200	V	
Repetitive reverse voltage	V_{DSS}	Clamp diode	1200		
Gate-source voltage(+)	V	D-S short	22		
Gate-source voltage(-)	V_{GSS}	D-S SHOIL	-4		
Drain current *1	I _D	DC (T _c =60°C)	300		
	I _{DRM}	Pulse (T _c =60°C) 1ms *2	600		
Source current *1	I _S	DC (T _c =60°C) V _{GS} =18V	300	1	
	I _{SRM}	Pulse (Tc=60°C) 1ms V _{GS} =18V * ²	600	Α	
	I _{SRM}	Pulse (Tc=60°C) 10 μ s V _{GS} =0V * ²	600		
Forward curent	l _F	DC (T _c =60°C) V _{GS} =18V	300]	
(clamp diode) *1 I _{FRM}		Pulse (Tc=60°C) 1ms V _{GS} =18V * ²	600	1	
Total power disspation *3	Ptot	T _c =25°C	1360	W	
Max Junction Temperature	T _{jmax}		175		
Junction temperature	T _{jop}		-40 to150	°C	
Storage temperature	T _{stg}		-40 to125		
Isolation voltage	Visol	Terminals to baseplate, f=60Hz AC 1min.	2500	Vrms	
Mounting torque		Main Terminals : M6 screw	4.5	N · m	
	_	Mounting to heat shink: M5 screw	3.5		

^(*1) Case temperature (T_c) is defined on the surface of base plate just under the chips.

Waveform for switching test



^(*2) Repetition rate should be kept within the range where temperature rise if die should not exceed T_{i max}.

^(*3) T_j is less than $175^{\circ}C$

●Electrical characteristics (T_i=25°C)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
Static drain-source on-state voltage	V _{DS(on)}	I _C =300A, V _{GS} =18V	T _j =25°C	-	1.9	3.0	V
			T _j =125°C	-	2.7	-	
			T _j =150°C	-	3	4.5	
Drain cutoff current	I _{DSS}	V _{DS} =1200V, V _{GS} =0V		ı	-	10	μΑ
Forwad Voltag	V _F	I _F =300A	T _j =25°C	ı	1.6	2.1	V
			T _j =125°C		2.2	-	
			T _j =150°C	1	2.3	3.2	
Reverse curent	I _{RRM}	Clamp diode		1	-	3.2	mA
Gate-source threshold voltage	$V_{GS(th)}$	V_{DS} =10V, I_{D} =80mA		2.7	-	5.6	V
Gate-source leakage current	I _{GSS}	V _{GS} =22V, V _{DS} =0V		1	-	0.5	μА
		V_{GS} = -6V, V_{DS} =0V		-0.5	-	-	
Switching characteristics	t _{d(on)}	$V_{GS(on)}$ =18V, $V_{GS(off)}$ =0	1	40	-	ns	
	t _r	V_{DS} =600 V I_{D} =300 A R_{G} =1.8 Ω		ı	35		-
	t _{rr}			-	6		-
	t _{d(off)}			1	155		-
	t _f	inductive load	ı	40	-		
Input capacitance	Ciss	V _{DS} =10V, V _{GS} =0V,100kHz		1	15	-	nF
Gate Registance	R_{Gint}	T _j =25°C		ı	0.9	-	Ω
NTC Rated Resistance	R25				5.0		kΩ
NTC B Value	B50/25				3370		K
Stray Inductance	Ls				13	-	nΗ
Creepage Distance	1	Terminal to heat sink			14.5	-	mm
		Terminal to terminal			15.0	-	mm
Clearance Distance	ı	Terminal to heat sink			12.0	-	mm
		Terminal to terminal			9.0	-	mm
Junction-to-case thermal resistance	R _{th} (j-c)	UMOS (1/2 module) *4		1	-	0.11	°C/W
		SBD (1/2 module) *4		-	-	0.11	
Case-to-heat sink Thermal resistance	R _{th} (c-f)	Case to heat sink, per	1 module,	- 0.035			
		Thermal grease appie	d * ⁵		0.035		

^(*4) In order to prevent self turn-on, it is recommended to apply negative gate bias.

^(*5) Measurement of Tc is to be done at the point just under the chip.

^(*6) Typical value is measured by using thermally conductive grease of λ =0.9W/(m·K).

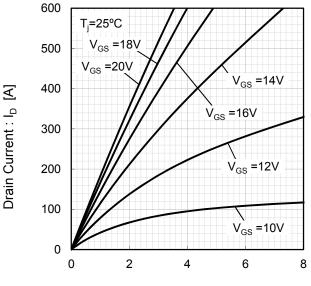
^(*7) SiC devices have lower short cuicuit withstand capability due to high current density.

Please be advised to pay careful attention to short cuicuit accident and try to adjust protection time to shutdown them as short as possible.

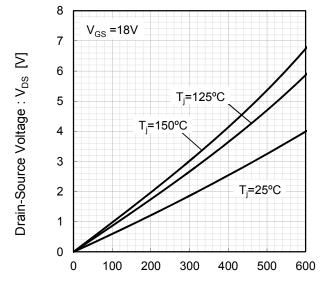
^(*8) If the Product is used beyond absolute maximum ratings defined in the Specifications, as its internal structure may be dameged, please replace such Product with a new one.

● Electrical characteristic curves (Typical)

Fig.1 Typical Output Characteristics [T_i =25°C] Fig.2 Drain-Source Voltage vs. Drain Current



Drain-Source Voltage : V_{DS} [V]



Drain Current : I_D [A]

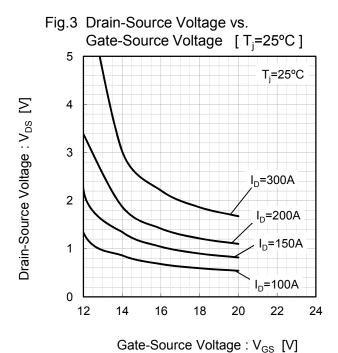
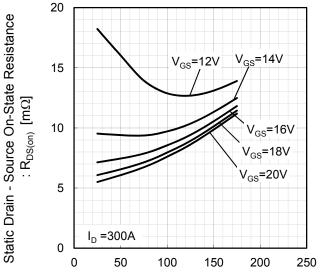
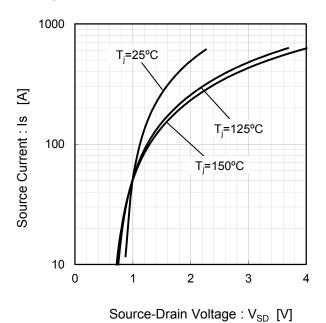


Fig.4 Static Drain - Source On-State Resistance vs. Junction Temperature



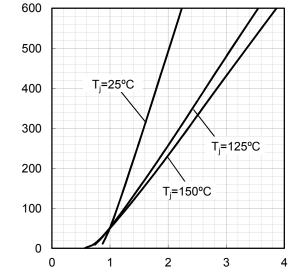
• Electrical characteristic curves (Typical)

Fig.5 Forward characteristic of Diode



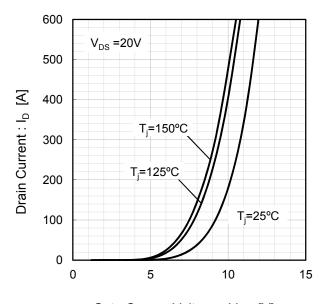
Source Current: Is [A]

Fig.6 Forward characteristic of Diode



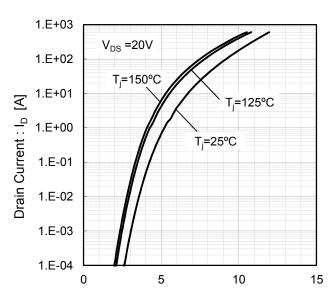
Source-Drain Voltage : V_{SD} [V]

Fig.7 Drain Current vs. Gate-Source Voltage



Gate-Source Voltage : V_{GS} [V]

Fig.8 Drain Current vs. Gate-Source Voltage



Gate-Source Voltage : V_{GS} [V]

●Electrical characteristic curves (Typical)

Fig.9 Switching Characteristics [T_i=25°C]

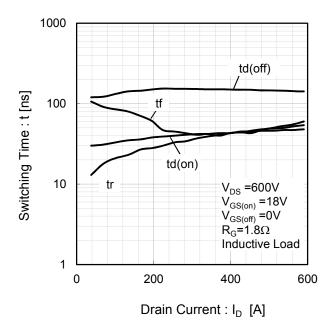


Fig.10 Switching Characteristics [T_i=125°C]

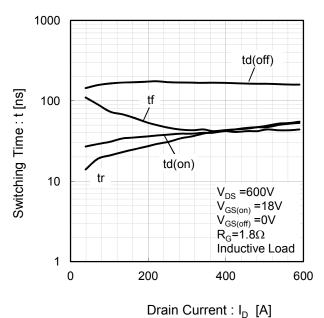


Fig.11 Switching Characteristics [T_i=150°C]

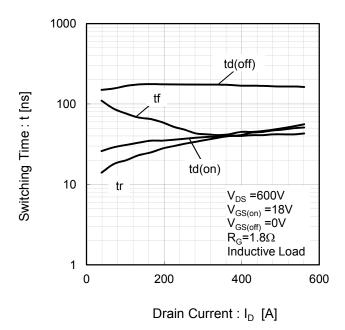
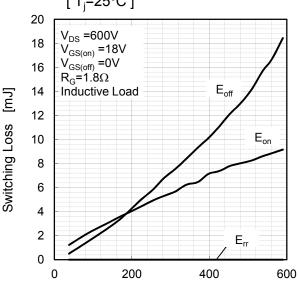


Fig.12 Switching Loss vs. Drain Current [T_i =25°C]



Drain Current : I_D [A]

●Electrical characteristic curves (Typical)

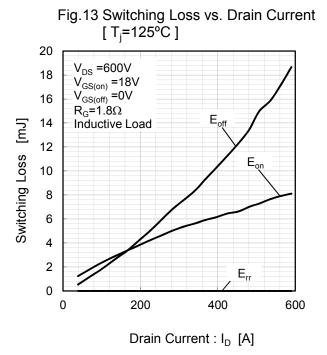


Fig.14 Switching Loss vs. Drain Current [T_i=150°C] 20 V_{DS} =600V 18 $V_{GS(on)} = 18V$ $V_{GS(off)} = 0V$ $R_G = 1.8\Omega$ 16 Switching Loss [mJ] 14 Inductive Load 12 10 8 6 4 2 E_{rr} 0 0 200 400 600 Drain Current : I_D [A]

Fig.15 Recovery Characteristics vs. Fig.16 Recovery Characteristics vs. Drain Current [T_i=25°C] Drain Current [T_i=125°C] 100 1000 1000 100 V_{DS} =600V V_{DS} =600V $V_{GS(on)} = 18V$ $V_{GS(off)} = 0V$ $R_G = 1.8\Omega$ Inductive Load $V_{GS(on)} = 18V$ $V_{GS(off)} = 0V$ $R_G = 1.8\Omega$ Recovery Current : I_{rr} [A] Recovery Current: Ir [A] Recovery Time : t_{rr} [ns] Recovery Time: trr [ns] Inductive Load 10 100 10 trr trr Irr Irr 10 1 1 10 0 100 200 300 400 500 600 0 100 200 300 400 500 600 Drain Current : I_D [A] Drain Current : I_D [A]

●Electrical characteristic curves (Typical)

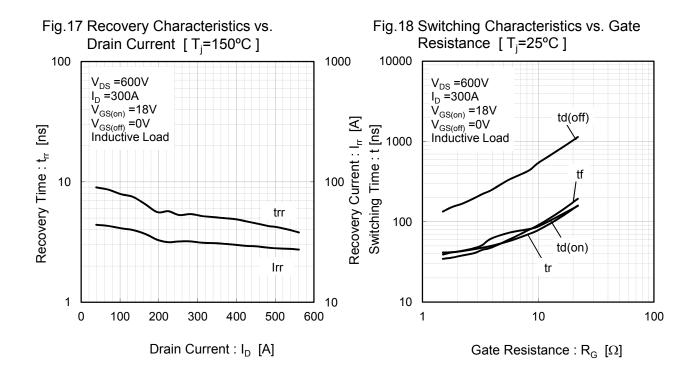


Fig.19 Switching Characteristics vs. Gate Resistance [T_i =125°C]

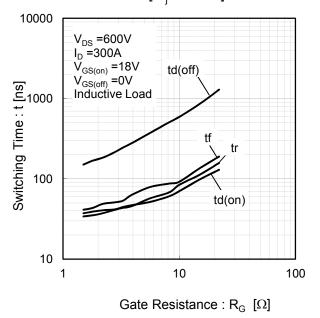
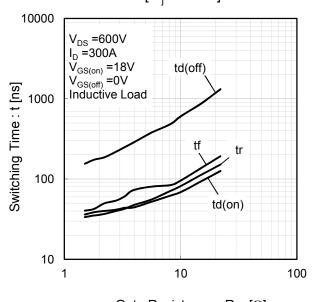


Fig.20 Switching Characteristics vs. Gate Resistance [T_i=150°C]



• Electrical characteristic curves (Typical)

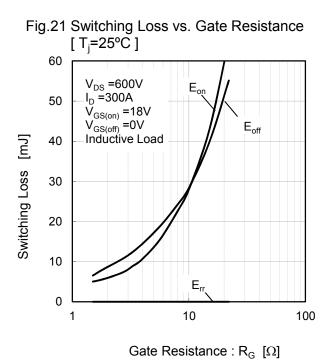
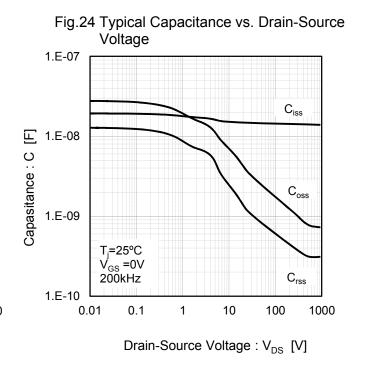


Fig.22 Switching Loss vs. Gate Resistance [T_i=125°C] 60 $\begin{aligned} &\mathsf{V}_{\mathrm{DS}} = \!\! 600 \mathsf{V} \\ &\mathsf{I}_{\mathrm{D}} = \!\! 300 \mathsf{A} \\ &\mathsf{V}_{\mathrm{GS(on)}} = \!\! 18 \mathsf{V} \\ &\mathsf{V}_{\mathrm{GS(off)}} = \!\! 0 \mathsf{V} \\ &\mathsf{Inductive Load} \end{aligned}$ 50 <u>[</u> 40 Switching Loss 30 20 E_{on} 10 E, 0 10 100

Gate Resistance : R_G [Ω]

Fig.23 Switching Loss vs. Gate Resistance [T_i=150°C] 60 V_{DS} =600V I_D =300A 50 $V_{GS(off)} = 18V$ $V_{GS(off)} = 0V$ Inductive Load Switching Loss [mJ] 40 30 20 E_{on} 10 E_{rr} 0 10 100 Gate Resistance : R_G [Ω]



● Electrical characteristic curves (Typical)

Fig.25 Gate Charge Characteristics

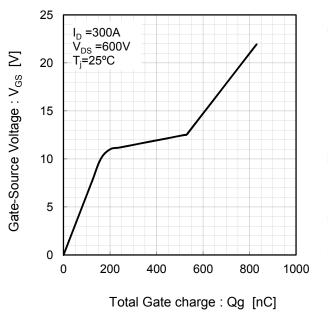


Fig.26 Normalized Transient Thermal Impedance 1 Normalized Transient Thermal Impedance: Rth 0.1 Single Pulse

0.01

0.001

0.01

Time [s]

Per unit base

0.1

UMOS part: 0.11K/W SBD part : 0.11K/W

T_c=25°C

10

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Package	E
Unit Quantity	4
Minimum Package Quantity	4
Packing Type	Corrugated Cardboard
Constitution Materials List	inquiry
RoHS	Yes